

LCD MODULE
SPECIFICATION

Model:	UE043WV-RB40-A037A
Version:	V1.0
Date:	20201112

☒ Preliminary Specification 样品规格书☐ Final Specification 量产规格书

Customer Confirmation 客户确认

Approved by	Notes

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VIEWE Confirmation 优奕确认

Prepared by	Reviewed by	Approved by

REVISION HISTORY

Revision 版本号	Date 日期	Contents of Revision Change 修改内容	Remark 备注
V1.0	20201112	Preliminary release	

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1. GENERAL INFORMATION

1.1 Features

- 1) Pixel Arrangement: RGB Vertical Stripe
- 2) Interface Mode: 40PIN RGB 24bits
- 3) Driver IC: ST7262E43-G4
- 4) Operation Temperature: -20~70℃
- 5) Storage Temperature: -30~80℃
- 6) Backlight Type: White LED
- 7) Display mode: Normally black, Transmissive
- 8) Pixel Density: 217 PPI
- 9) LED life time: 30,000 Hours

1.2 Mechanical Specification

Item 项目	Specification 规格	Unit 单位	Remark 备注
Pixel Driving element	A-Si TFT	-	-
Screen Size	4.3	Inch	Diagonal
Resolution	800(W)*3(RGB)*480(H)	Dots	-
Interface	RGB 24bits	-	40PIN
Module Power Consumption	1.04	Watt	Typ.
Active Area	95.04(W)*53.86(H)	mm	-
Pixel pitch (W*H)	0.1188(W)*0.1122(H)	mm	-
Module Size (W*H*D)	105.52(W)*67.17(H)*2.8(D)	mm	Tolerance: ±0.2
Luminance	350	cd/m ²	Typ.
Viewing Direction	All	O'clock	-
Display Color	16.2M	Colors	24bits

2. ABSOLUTE MAXIMUM RATINGS

Item 项目	Symbol 符号	Min. 最小值	Max. 最大值	Unit 单位	Remark 备注
Power supply voltage	VDD	-0.5	3.96	V	Note1
LED forward current	I _F	-0.001	30	mA	For each led, Note1
LED Reverse Voltage	V _R	-	5	V	For each led, Note1
Operating temperature	T _{op}	-30	85	°C	Note1,2
Storage temperature	T _{st}	-40	85	°C	Note1,2
Humidity	H _{st}	10	90	%RH	Note1,3

(T_a=+25°C, GND=0V)

Note1: If the module exceeds the absolute maximum ratings, it may be damaged permanently. Also if the module operates with the absolute maximum ratings for a long time, the reliability may drop.

Note2: In case of temperature below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one.

Note3: Temp. ≤ 60°C , 90% RH MAX.

Temp. > 60°C , Absolute humidity shall be less than 90% RH at 60°C.

4. I/O CONNECTION & BLOCK DIAGRAM

4.1 I/O Connection

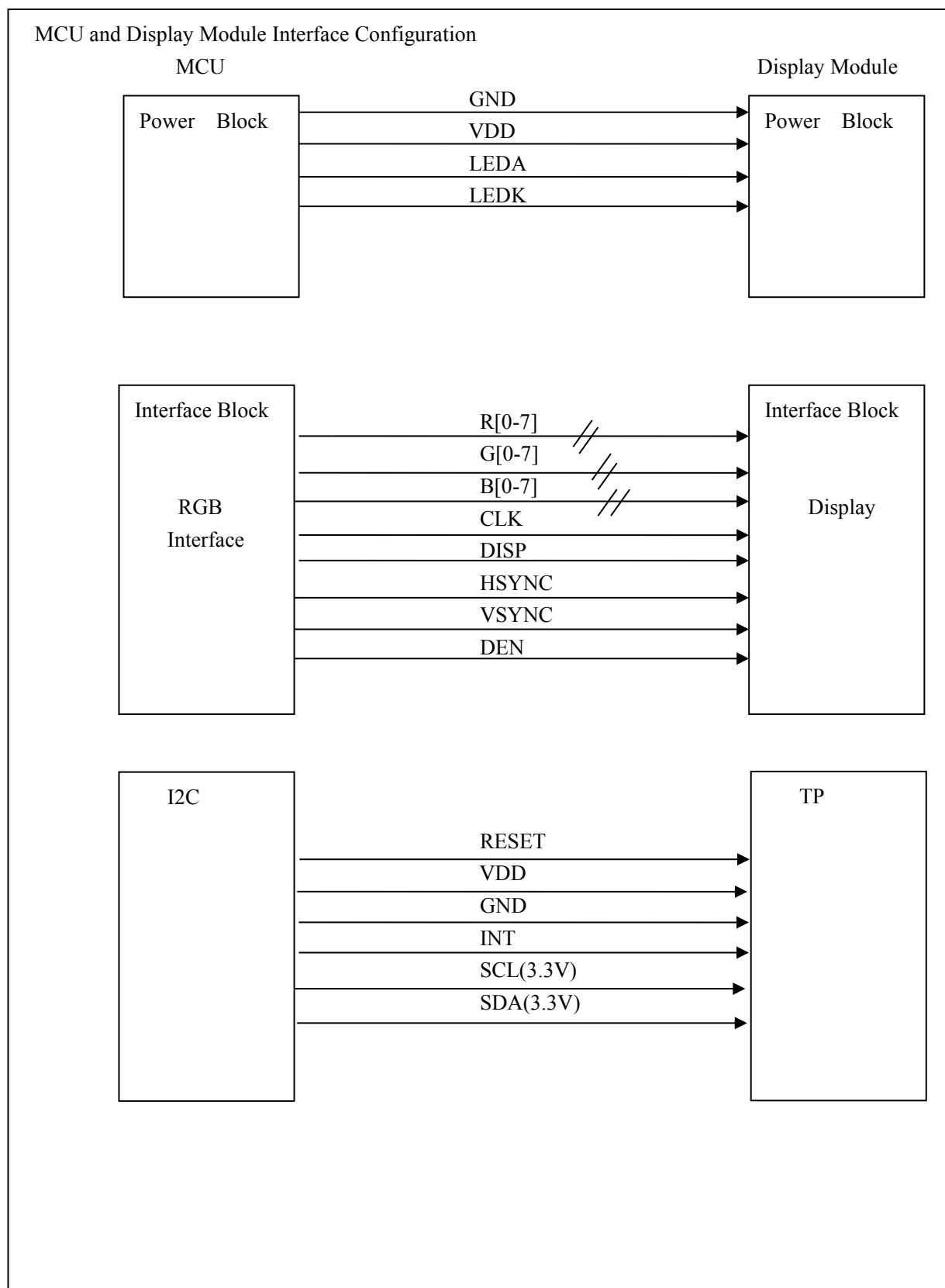
Pin No. 序号	Symbol 符号	I/O	Description 描述
1	LEDK	P	Power supply for backlight cathode
2	LEDA	P	Power supply for backlight anode
3	GND	P	Power Ground
4	VDD	P	Power supply to the internal logic power regulator(3.3V)
5-12	R0-R7	I	Red data input.
13-20	G0-G7	I	Green data input.
21-28	B0-B7	I	Blue data input.
29	GND	P	Power Ground
30	CLK	I	Pixel clock input pin, Negative polarity
31	DISP	I	Standby mode. Normally pulled high.
32	HSYNC	I	Horizontal sync signal, Negative polarity
33	VSYNC	I	Vertical sync signal, Negative polarity
34	DEN	I	Data input enable. Display access is enabled when DE is “H”
35	NC	I	Dummy
36	GND	P	Power Ground
37	XR	-	Dummy
38	YD	-	Dummy
39	XL	-	Dummy
40	YU	-	Dummy

I: Input; O: Output; P: Power

4.2 TP Connection

Pin No. 序号	Symbol 符号	I/O	Description 描述
1	RESET	I	The signal will reset the TP,Signal is active low
2	VDD	P	Power supply for TP
3	GND	P	Power Ground
4	INT	O	Interrupt signals for TP
5	SCL(3.3 V)	I	I2C clock signals for TP
6	SDA(3.3 V)	I/O	I2C data signal for TP

4.3 Block Diagram



5. ELECTRICAL CHARACTERISTICS

5.1 TFT-LCD Panel Driving Section

Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Power Supply Voltage	VDD	3.0	3.3	3.6	V	-
Power Supply Current	I _{VDD}	-	122	-	mA	Note1
Logic Input High Voltage	V _{IH}	0.7VDD	-	VDD	V	-
Logic Input Low Voltage	V _{IL}	0	-	0.3VDD	V	-
Panel Power Consumption	P _{VDD}	-	0.4	-	Watt	Note1
Module Power Consumption	P _{LCM}	-	1.04	-	Watt	Note1,2

(Ta=+25°C,GND=0V)

Note1: Measurement Conditions (Video Mode): Full Screen Red Pattern, VDD=3.3V, 60Hz Refresh.

Note2: P_{LCM}= P_{VDD}+ P_{BL}, About P_{BL} information, inference to 5.2 Back Light Driving Section.

5.2 Back Light Driving Section

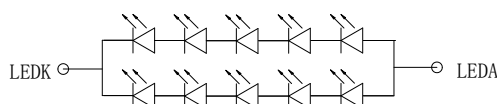
Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Forward Voltage	V _F	-	16	17	V	Note1
Forward Current	I _F	-	40	-	mA	Note1
Backlight Power consumption	P _{BL}	-	0.64	-	Watt	Note1
LED life time	-	30000	-	-	Hrs	Note2
LED Quantity		10			PCS	

(Ta=+25°C,GND=0V)

Note1: The LED driving condition is defined for each LED module (5 LED Serial, 2 LED Parallel)。

For each LED : I_F=20mA, V_F=3.2V(Typ.)/3.4V(Max.), Ta=25°C。

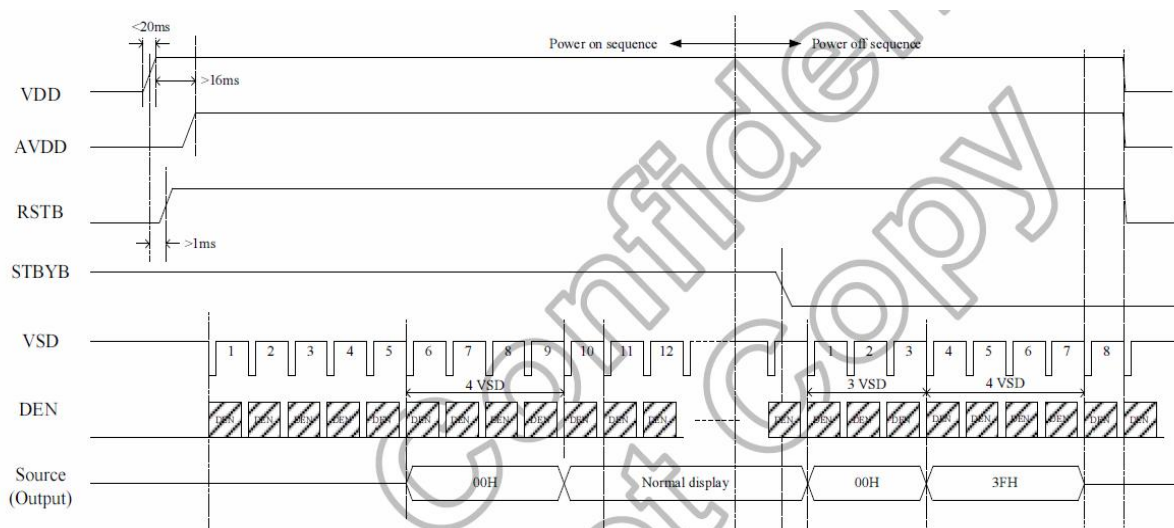
Note2: The “LED life time” is defined as the module brightness decrease to 50% of original brightness at I_{LED}=20mA(Per Led). The LED life time could be decreased if operating I_{LED} is larger than 20mA.



LED CIRCUIT DIAGRAM

V_f=14-17V (I_f=40mA)

5.3 Power On/Off Sequence



5.4 Timing Characteristics

5.4.1 Timing Parameters

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
HS setup time	T_{hst}	8	-	-	ns
HS hold time	T_{hhd}	8	-	-	ns
VS setup time	T_{vst}	8	-	-	ns
VS hold time	T_{vhd}	8	-	-	ns
Data setup time	T_{dsu}	8	-	-	ns
Data hold time	T_{dhd}	8	-	-	ns
DE setup time	T_{esu}	8	-	-	ns
DE hold time	T_{ehd}	8	-	-	ns
VDD Power On Slew rate	T_{POR}	-	-	20	ms
RSTB pulse width	T_{Rst}	10	-	-	us
CLKIN cycle time	T_{cph}	20	-	-	ns
CLKIN pulse duty	T_{cwh}	40	50	60	%
Output stable time	T_{sst}	-	-	6	us

($T_a = +25^\circ\text{C}$, $GND = 0V$)

5.4.2 Horizontal Timing

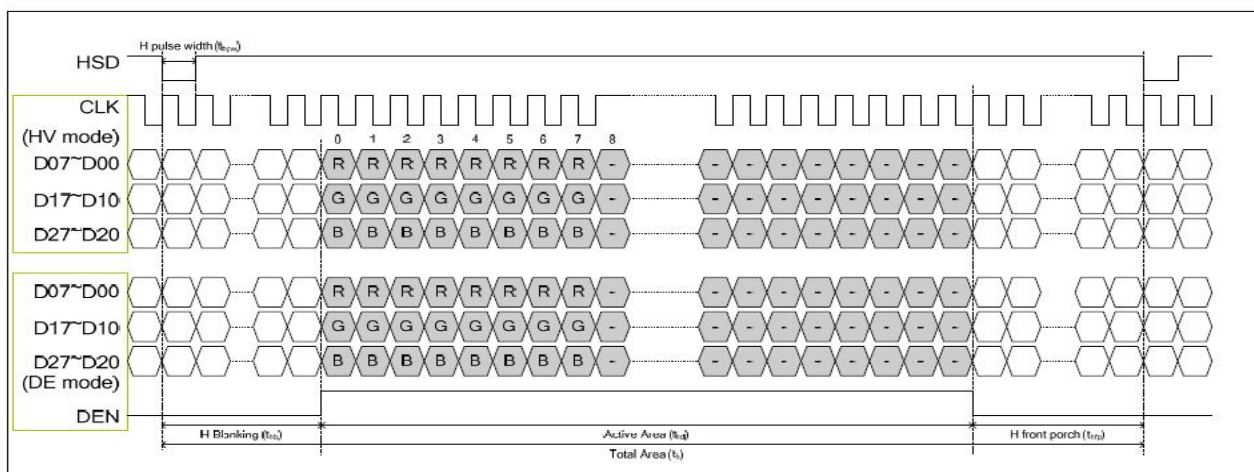
Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Horizontal Display Area	thd	-	800	-	DCLK
DCLK frequency	fclk	-	30	50	MHz
One Horizontal Line	th	889	928	1143	DCLK
HS pulse width	thpw	1	48	255	DCLK
HS Back Porch (Blanking)	thb	-	88	-	DCLK
HS Front Porch	thfp	1	40	255	DCLK
DE mode Blanking	th-thd	85	128	512	DCLK

5.4.3 Vertical Timing

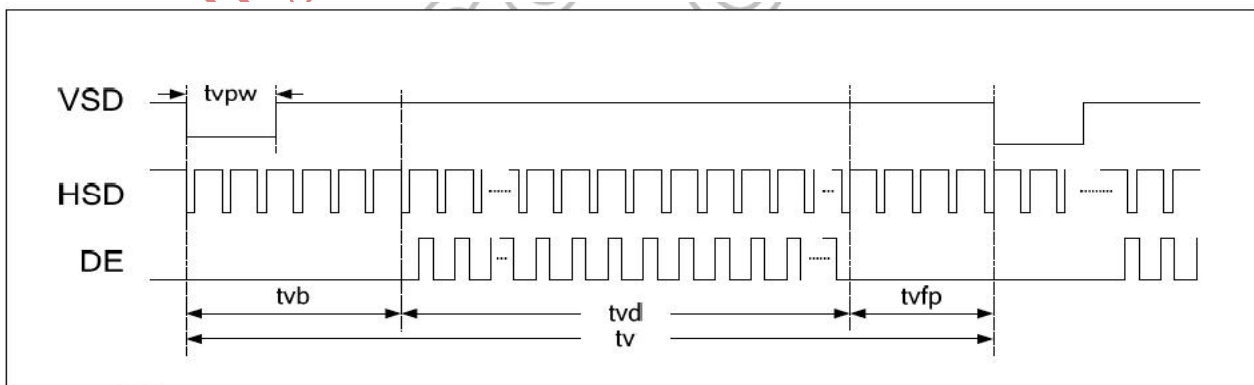
Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd		480		T _H
VS period time	tv	513	525	767	T _H
VS pulse width	tvpw	3	3	255	T _H
VS Back Porch (Blanking)	tvb		32		T _H
VS Front Porch	tvfp	1	13	255	T _H
DE mode Blanking	tv-tvd	4	45	255	T _H

5.5 Timing Diagram

5.5.1 Horizontal Timing



5.5.2 Vertical Timing



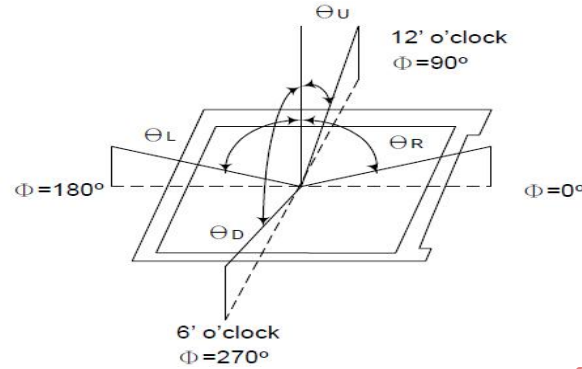
6. OPTICAL CHARACTERISTICS

Parameter 参数	Symbol 符号	Condition 条件	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Contrast Ratio	C/R	$\theta = 0^\circ$	640	800	-	-	Note(4)
NTSC Ratio	S	$\theta = 0^\circ$	45	50	-	%	Note(7)
Luminance	L	$\theta = 0^\circ$	300	350	-	cd/m ²	Note(5)
Luminance uniformity	U _w	$\theta = 0^\circ$	70	80	-	%	Note(3)
Response Time	T _R + T _F	25 °C	-	30	40	ms	Note(2)
Color Coordination	W _X	$\theta = 0^\circ$ (Center) Normal viewing angle B/L On	-0.04	0.33	+0.04	NTSC (x,y)	Note(6)
	W _Y			0.35			
	R _X			0.631			
	R _Y			0.329			
	G _X			0.328			
	G _Y			0.548			
	B _X			0.136			
	B _Y			0.141			
Viewing Angle	θ_L	C/R>10	70	80	-	Degree	Note(1)
	θ_R		70	80	-		
	θ_U		70	80	-		
	θ_D		70	80	-		

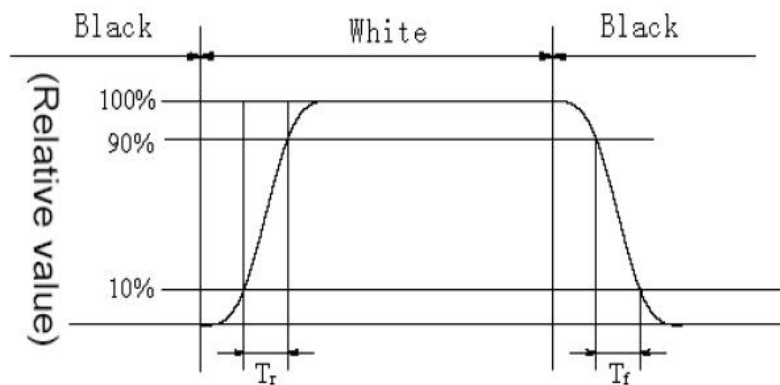
Test Conditions:

1. VDD=3.3V, I_F=40mA (Backlight current), the ambient temperature is +25℃.
2. The test systems refer to Note 8.

Note1: Definition of Viewing Angle: The viewing angle range that the CR>10

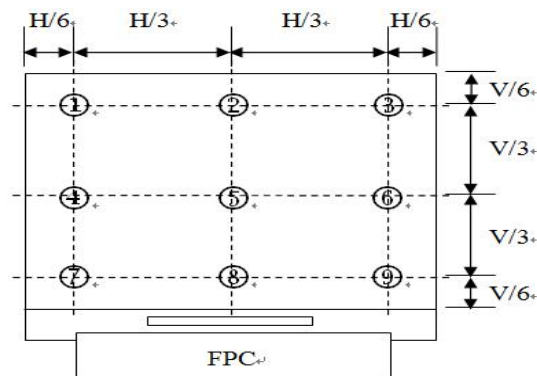


Note2: Definition of Response time: Sum of T_R and T_F



Note 3: Definition of Luminance Uniformity: Active area is divided into 9 measuring areas, every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$



Note4: Definition of Contrast Ratio (CR): measured at the center point of panel

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

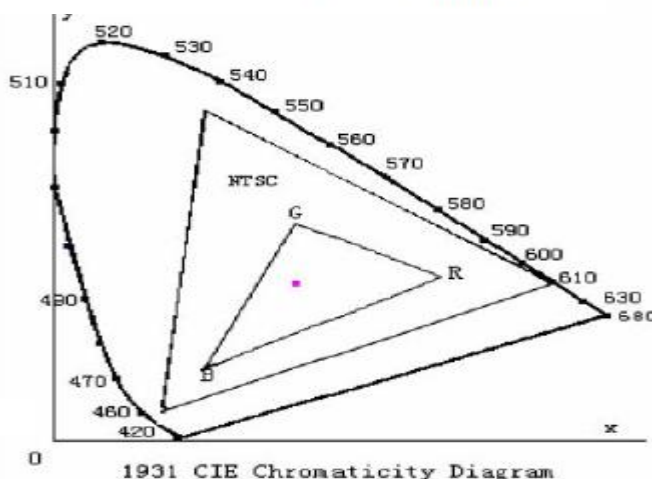
Note 5: Definition of Luminance: Center Luminance of white is defined as luminance values of 1point average across the LCD surface.

Note 6: Definition of Color Chromaticity (CIE 1931)

Color coordinates of white & red, green, blue measured at center point of LCD.

Note 7: Definition of NTSC ratio:

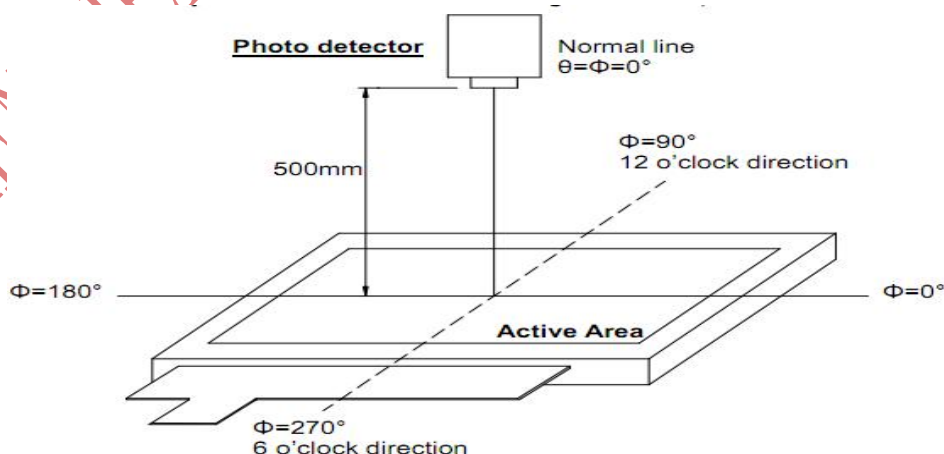
$$\text{NTSC ratio} = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}}$$



Note 8: Definition of measurement system.

optical

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, Field of view: 1°/Height: 500mm.)



7. RELIABILITY

Item 项目	Test Condition 测试条件	Remark 备注
High Temperature Storage	Ta =+80°C / 240Hours	Note1,2,3
Low Temperature Storage	Ta =-40°C / 240Hours	Note1,2,3
High Temperature Operating	Ta =+70°C / 240Hours	Note1,2,3
Low Temperature Operating	Ta =-30°C / 240Hours	Note1,2,3
Temperature Cycle storage Test	-30°C/30min ↔ +80°C /30min for 30cycles, Transfer time less than 5min	Note2,3
Thermal humidity storage Test	60°C x 90%RH / 240Hours	Note2,3
Package Vibration Test	Frequency: 10Hz~55Hz, Amplitude: 1.5mm, 1 hrs for each direction of X, Y, Z	Note2
Packing shock test	Drop to the ground from 60cm height, 1 corner, 3 edges, 6 surfaces.	Note2

Inspection after Test:

Note1: Ta is the ambient temperature of samples.

Note 2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.

Note 3: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

8. PACKAGE DRAWING

