

LCD MODULE
SPECIFICATION

Model:	UE015HV-RB40-L006-A
Version:	V1.0
Date:	20190605

☒ **Preliminary Specification** 样品规格书

☐ **Final Specification** 量产规格书

Customer Confirmation 客户确认

Approved by	Notes

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VIEWE Confirmation 优奕确认

Prepared by	Reviewed by	Approved by

REVISION HISTORY

Revision 版本号	Date 日期	Contents of Revision Change 修改内容	Remark 备注
V1.0	2019.06.05	Preliminary release	

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1. GENERAL INFORMATION

1.1 Features

- 1) Pixel Arrangement: RGB Vertical Stripe
- 2) Interface Mode: SPI_3+RGB_16bit
- 3) Driver IC: ST7796S-G5
- 4) Operation Temperature: -20~70℃
- 5) Storage Temperature: -30~80℃
- 6) Backlight Type: White LED
- 7) Display mode: Normally Black
- 8) Pixel Density: 293 PPI
- 9) LED life time: 30,000 Hours

1.2 Mechanical Specification

Item 项目	Specification 规格	Unit 单位	Remark 备注
Pixel Driving element	IPS TFT	-	
Screen Size	1.54	Inch	Diagonal
Resolution	320(W)*3(RGB)*320(H)	Dots	
Interface	SPI	-	3 lane
Module Power Consumption	0.3	Watt	Typ.
Active Area	27.74(W)*27.74(H)	mm	
Pixel pitch (W*H)	0.0867(W)*0.0867(H)	mm	
Module Size (W*H*D)	30.52(W)*33.72(H)*1.37(D)	mm	Tolerance: ±0.1
Luminance	520	cd/m ²	Typ.
Viewing Direction	All	O'clock	-
Display Color	16.7M(Dithering)	Colors	-

2. ABSOLUTE MAXIMUM RATINGS

Item 项目	Symbol 符号	Min. 最小值	Max. 最大值	Unit 单位	Remark 备注
Power supply1 voltage	VDD	-0.3	4.6	V	Note1
LED forward current	I _F	-0.001	30	mA	For each led,Note1
LED Reverse Voltage	V _R	-	5	V	For each led,Note1
Operating temperature	T _{op}	-20	70	°C	Note1,2
Storage temperature	T _{st}	-30	80	°C	Note1,2
Humidity	H _{st}	10	90	%RH	Note1,3

(Ta=+25°C,GND=0V)

Note1:If the module exceeds the absolute maximum ratings, it may be damaged permanently. Also if the module operates with the absolute maximum ratings for a long time, the reliability may drop.

Note2: In case of temperature below 0°C,the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one.

Note3: Temp. ≤ 60°C , 90% RH MAX.

Temp. > 60°C , Absolute humidity shall be less than 90% RH .

3. MECHANICAL DRAWING

[illegible]

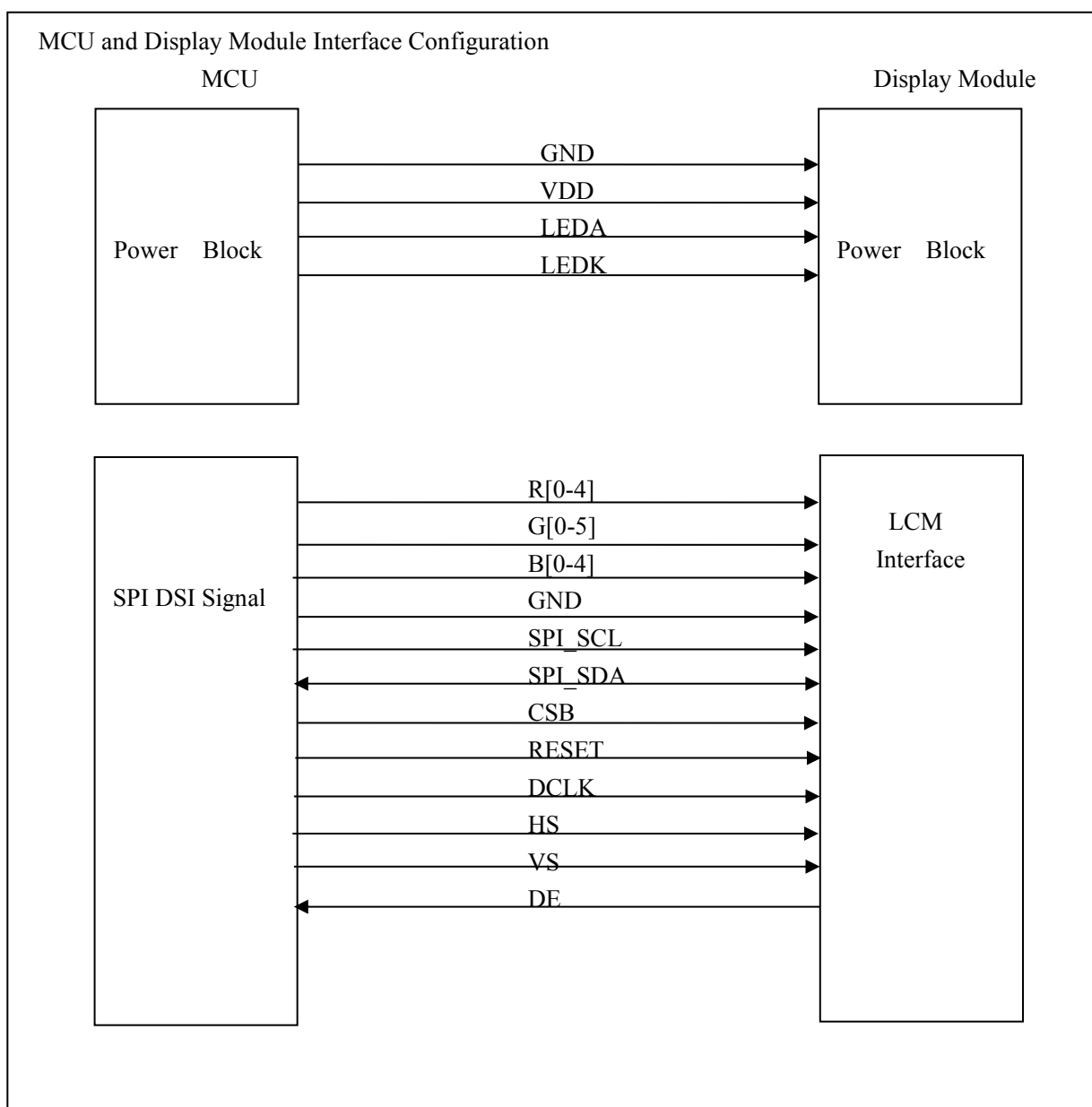
4. I/O CONNECTION & BLOCK DIAGRAM

4.1 I/O Connection

Pin No. 序号	Symbol 符号	I/O	Description 描述
1	LED_K	P	Power for Backlight Cathode
2	LED_A	P	Power for Backlight Anode
3	GND	P	Power Ground
4	VDD	P	Power supply for analog circuit(2.8V).
5-6	NC	-	Dummy
7-11	B0-B4	I	Blue data input
12-13	NC	-	Dummy
14-19	B5-B10	I	Green data input.
20-21	NC	-	Dummy
22-26	B11-B15	I	Red data input.
27-28	NC	-	Dummy
29	GND	P	Power Ground
30	DCLK	I	Pixel clock input pin, Negative polarity
31	NC	-	Dummy
32	HSYNC	I	Horizontal sync signal, Negative polarity
33	VSNC	I	Vertical sync signal, Negative polarity
34	DE	I	Data input enable. Display access is enabled when DE is “H”
35	RESET	I	Global reset for LCM, active is low
36	GND	P	Power Ground
37	SDA	I/O	Serial in/out signal, for initial RGB I/F.
38	SCL	I	serial interface clock, for initial RGB I/F.
39	CSB	I	Chip select input pin (“Low” enable), for initial RGB I/F.
40	NC	-	Dummy

I: Input; O: Output; P: Power

4.2 Block Diagram



5. ELECTRICAL CHARACTERISTICS

5.1 TFT-LCD Panel Driving Section

Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Power Supply1 Voltage	VDD	2.5	3.3	3.6	V	-
Power Supply Current	IVDD	-	30	-	mA	Note1
Logic Input High Voltage	V _{IH}	0.7VDD	-	VDD	V	-
Logic Input Low Voltage	V _{IL}	0	-	0.3VDD	V	-
Panel Power Consumption	P _{VDD}	-	0.099	-	Watt	Note1
Module Power Consumption	P _{LCM}	-	0.3	-	Watt	Note1,2

(Ta=+25°C,GND=0V)

Note1:Measurement Conditions (Video Mode): Full Screen Red Pattern,VDD=2.8V,60Hz Refresh.

Note2: P_{LCM}= P_{VDD}+ P_{BL}, About P_{BL} information, inference to 5.2 Back Light Driving Section.

5.2 Back Light Driving Section

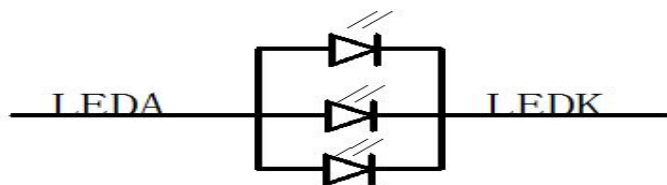
Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Forward Voltage	V _F	-	3.3	-	V	Note1
Forward Current	I _F	-	60	-	mA	Note1
Backlight Power consumption	P _{BL}	-	0.192	-	Watt	Note1
LED life time	-	30000	-	-	Hours	Note2
LED Quantity		3			PCS	

(Ta=+25°C,GND=0V)

Note1: The LED driving condition is defined for each LED module (3 LED Serial, 1 LED Parallel)。

For each LED : I_F=20mA,V_F=9.6V(Typ.),Ta=25°C。

Note2:The “LED life time” is defined as the module brightness decrease to 50% of original brightness at I_{LED}=20mA(Per Led). The LED life time could be decreased if operating I_{LED} is larger than 20mA.



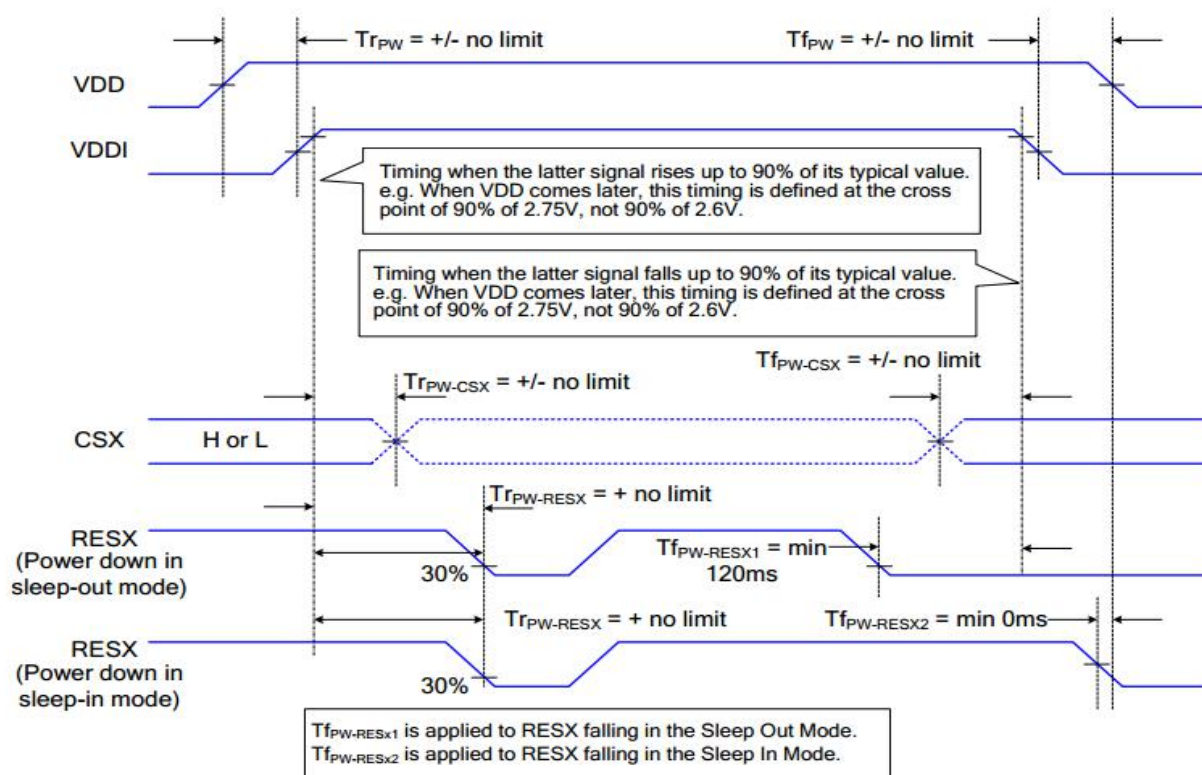
LED V_f : 3.2V (Typ)

I_f : 60mA

背光电路图

5.3 Power On/Off Sequence

5.3.1 Power On Sequence



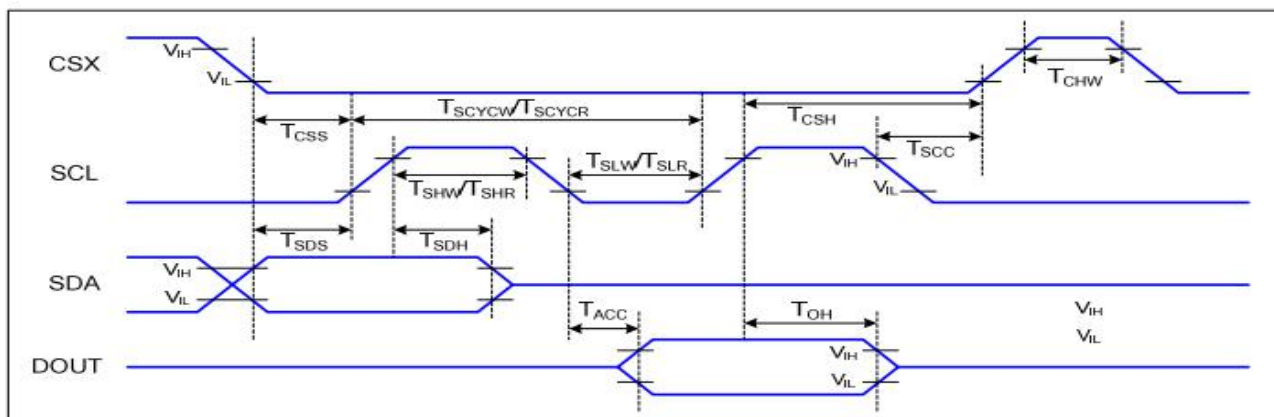
5.3.2 Power Off Sequence

The uncontrolled power-off means a situation which removed a battery without the controlled power off sequence. It will neither damage the module or the host interface.

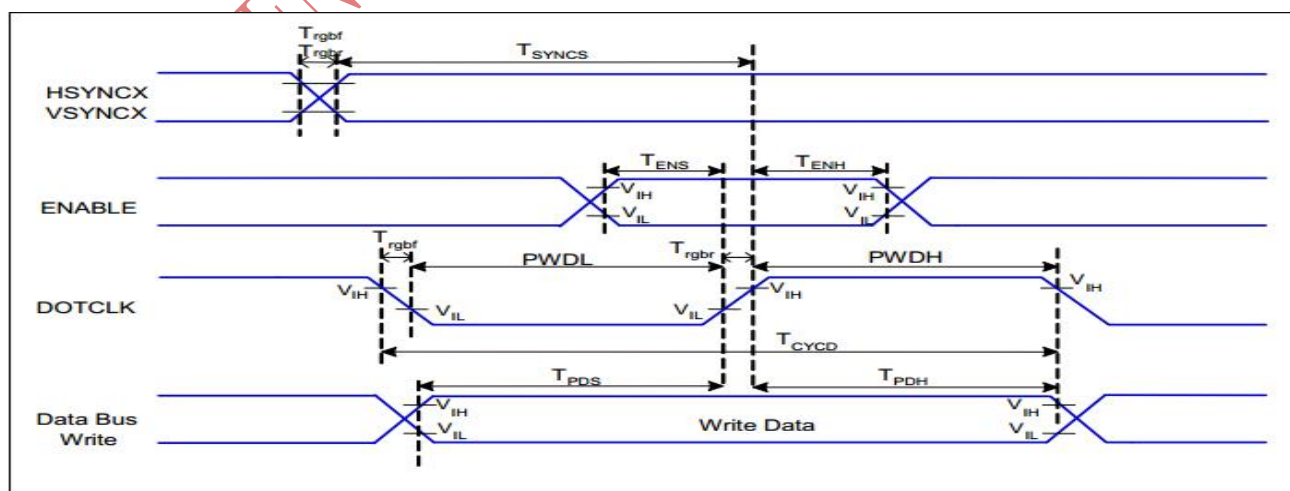
If uncontrolled power-off happened, the display will go blank and there will not any visible effect on the display (blank display) and remains blank until "Power On Sequence" powers it up.

5.4 Timing Characteristics

5.4.1 SPI AC Characteristics



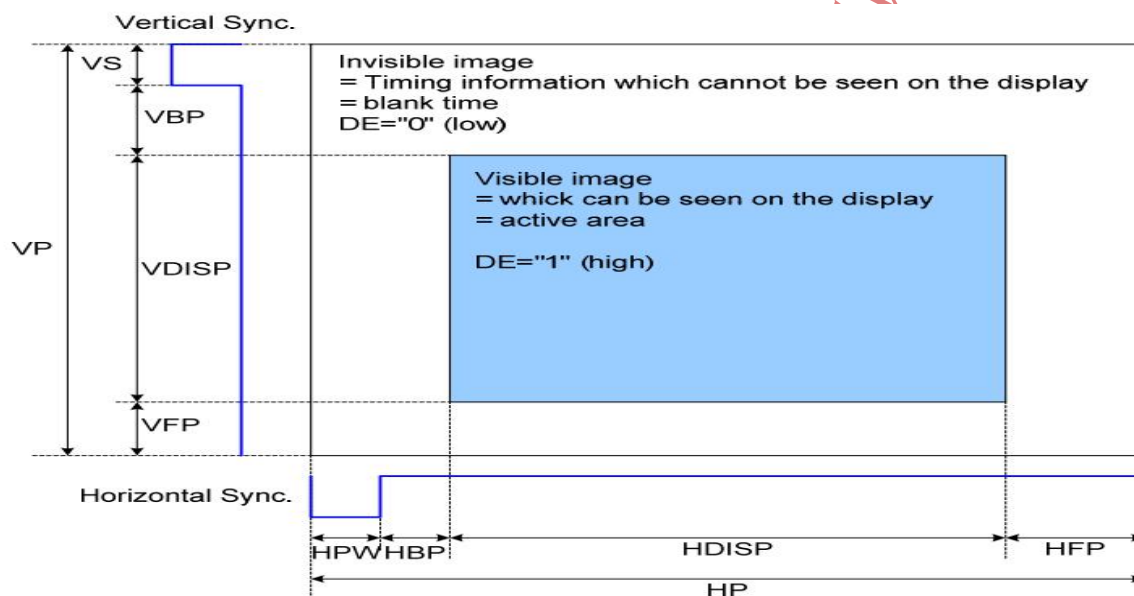
Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	65		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	66		ns	
	T_{SHW}	SCL "H" pulse width (Write)	15		ns	
	T_{SLW}	SCL "L" pulse width (Write)	15		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T_{SDS}	Data setup time	10		ns	
	T_{SDH}	Data hold time	10		ns	
DOUT	T_{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T_{OH}	Output disable time	15	50	ns	For minimum CL=8pF



Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYNC, HSYNC Setup Time	15	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	15	-	ns	
	T_{ENH}	Enable Hold Time	15	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	30	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	30	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	66	-	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	-	15	ns	
DB	T_{PDS}	PD Data Setup Time	15	-	ns	
	T_{PDH}	PD Data Hold Time	15	-	ns	

5.5 Timing Diagram

5.5.1 Horizontal + Vertical Timings



5.5.3 Timing Parameters

Parameter	Symbol	Value			Unit
		Min.	Typ.	MAX.	
Bit rate per lane	BRPHY	-	-	1000	Mbps
Active pixel per line	HACT	-	320	-	Pixels
Horizontal back porch	t _{HBP}	-	32	-	DCLK
Horizontal sync active	t _{HSA}	-	16	-	DCLK
Horizontal front porch	t _{HFP}	-	26	-	DCLK
Active pixel per frame	VACT	-	320	-	H
Vertical back porch	t _{VBP}	-	8	-	H
Vertical sync active	t _{VSA}	-	4	-	H
Vertical front porch	t _{VFP}	-	12	-	H

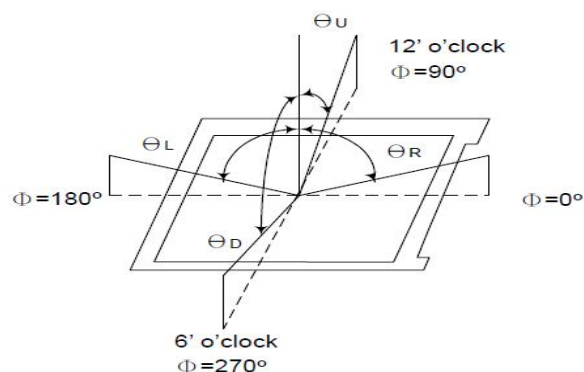
6. OPTICAL CHARACTERISTICS

Parameter 参数	Symbol 符号	Condition 条件	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Contrast Ratio	C/R	$\theta = 0^\circ$	-	700	-	-	Note(4)
NTSC Ratio	S	$\theta = 0^\circ$	-	50	-	%	Note(7)
Luminance	L	$\theta = 0^\circ$	-	520	-	cd/m ²	Note(5)
Luminance uniformity	U _W	$\theta = 0^\circ$	75	80	-	%	Note(3)
Response Time	T _R + T _F	25 °C	-	35	50	ms	Note(2)
Color Coordination	W _X	$\theta = 0^\circ$ (Center) Normal viewing angle B/L On	-0.03	0.295	+0.03	NTSC (x,y)	Note(6)
	W _Y			0.325			
	R _X			0.626			
	R _Y			0.310			
	G _X			0.296			
	G _Y			0.518			
	B _X			0.144			
	B _Y			0.139			
Viewing Angle	θ_L	C/R>10	70	80	-	Degree	Note(1)
	θ_R		70	80	-		
	θ_U		70	80	-		
	θ_D		70	80	-		

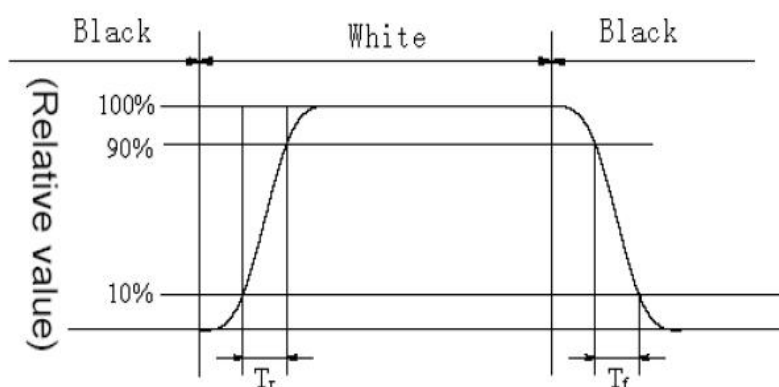
Test Conditions:

1. VDD=2.8V, I_F=40mA (Backlight current), the ambient temperature is +25°C.
2. The test systems refer to Note 8.

Note1: Definition of Viewing Angle: The viewing angle range that the CR>10

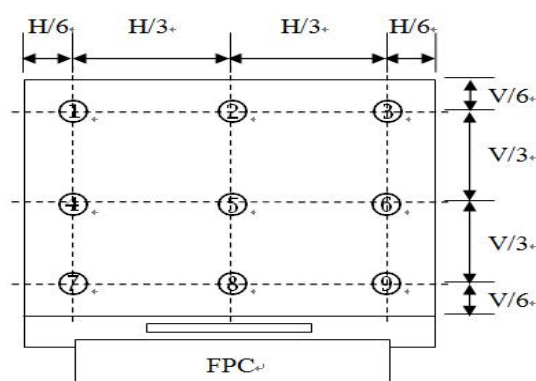


Note2: Definition of Response time: Sum of T_R and T_F



Note 3: Definition of Luminance Uniformity: Active area is divided into 9 measuring areas, every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$



Note4: Definition of Contrast Ratio (CR): measured at the center point of panel

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

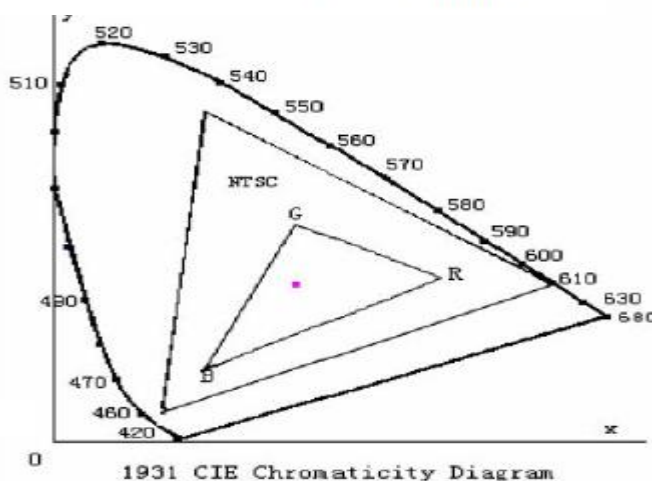
Note 5: Definition of Luminance: Center Luminance of white is defined as luminance values of 1 point average across the LCD surface.

Note 6: Definition of Color Chromaticity (CIE 1931)

Color coordinates of white & red, green, blue measured at center point of LCD.

Note 7: Definition of NTSC ratio:

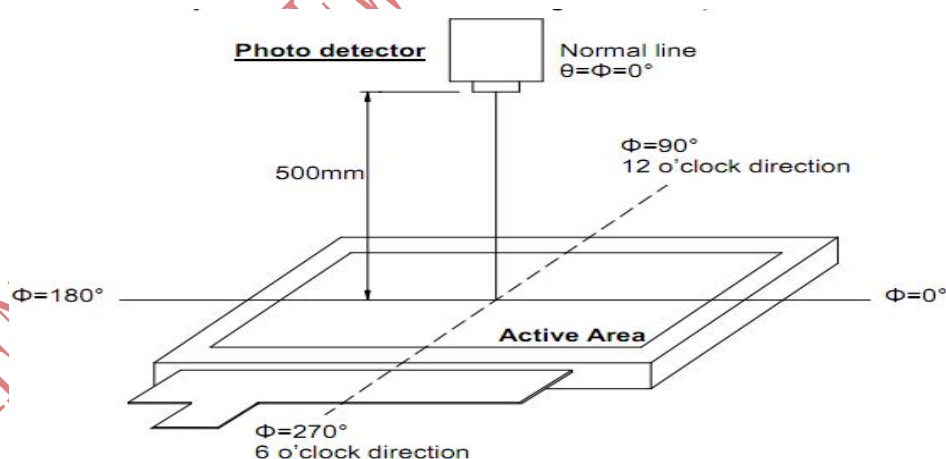
$$\text{NTSC ratio} = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}}$$



Note 8: Definition of measurement system.

optical

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, Field of view: 1°/Height: 500mm.)



7. RELIABILITY

Item 项目	Test Condition 测试条件	Remark 备注
High Temperature Storage	Ta =+80°C / 96Hours	Note1,2,3
Low Temperature Storage	Ta =-30°C / 96Hours	Note1,2,3
High Temperature Operating	Ta =+70°C / 96Hours	Note1,2,3
Low Temperature Operating	Ta =-20°C / 96Hours	Note1,2,3
Temperature Cycle storage Test	-30°C/30min ↔ +80°C /30min for 30cycles, Transfer time less than 5min	Note2,3
Thermal humidity storage Test	60°C x 90%RH / 96Hours	Note2,3
Package Vibration Test	Frequency: 10Hz~55Hz, Amplitude: 1.5mm, 1 hrs for each direction of X, Y, Z	Note2
Packing shock test	Drop to the ground from 60cm height, 1 corner, 3 edges, 6 surfaces.	Note2
ESD test	Contact: ±4KV Air: ±8KV	ESD

Inspection after Test:

Note1: Ta is the ambient temperature of samples.

Note 2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.

Note 3: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.

8. PACKAGE DRAWING

