

LCD MODULE SPECIFICATION

| Model: | LCM-UE046VQ-RB40-L001A |
|--|---|
| Version: | V1.0 |
| Date: | 20221023 |
| Customer Confirmation 客户硕 | |
| Approved by | Notes |
| | |
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VIEWE Confirmation 优奕确认

| Prepared by | Reviewed by | Approved by |
|-------------|-------------|-------------|
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REVISION HISTORY

| Revision 版本号 | Date 日期 | Contents of Revision Change 修改内容 | Remark 备注 |
|-----------------|------------|-------------------------------------|--------------|
| V1.0 | 2022.10.23 | Preliminary release | |
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1. GENERAL INFORMATION

IEWe

1.1 Features

- 1) Pixel Arrangement: RGB Vertical Stripe
- 2) Interface Mode: 3SPI-RGB 18bits
- 3) Driver IC: ST7701S or Equivalent
- 4) Operation Temperature: -20~70℃
- 5) Storage Temperature: -30~80°C
- 6) Backlight Type: White LED
- 7) Display mode: Normally Black
- 8) LED life time: 30,000 Hours

| 1.2 Mechanical Specificat | ion |) - | |
|----------------------------------|----------------------------|-------------------|--------------|
| Item 项目 | Specification 规格 | Unit 单位 | Remark 备注 |
| Pixel Driving element | A-Si TFT | - | |
| Screen Size | 4.58 | Inch | Diagonal |
| Resolution | 320(W)*3(RGB)*960(H) | Dots | |
| Interface | 3SPI_RGB 18bits | - | 40PIN |
| Module Power Consumption | TBD | Watt | Тур. |
| Active Area | 36.77 (H)* 110.30 (V) | mm | |
| Module Size (W*H*D) | 41.57 (H)*118.8 (V)*2.5(D) | mm | |
| Luminance | 300 | cd/m ² | Тур. |
| Viewing Direction | All | O'clock | _ |
| Display Color | 16.7M | Colors | 18bits |



2. ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Min | Max | Unit | Note |
|----------------------------|-----------|------|-----------|------|------|
| Power Supply voltage 1 | VCI~GND | -0.3 | +4.6 | V | |
| Power Supply voltage 2 | IOVCC~GND | -0.3 | +4.6 | V | |
| Logic Input Voltage Range | VIN | -0.3 | IOVCC+0.5 | V | |
| Logic Output Voltage Range | VO | -0.3 | IOVCC+0.5 | V | |
| Operating temperature | Topr | -20 | +70 | | |
| Storage temperature | Tstg | -30 | +80 | C | |

* The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

3. DC ELECTRICAL CHARACTERISTICS

3.1 Driving TFT LCD Panel

| | - C X | A | GND = | GND = 0V, Ta | = 25C |
|-----------------------------------|--------|-----------|-------|--------------|-------|
| Parameter | Symbol | Min | Тур | Max | Unit |
| Supply voltage for analog circuit | VCI | 2.5 | 2.8 | 3.6 | V |
| Supply voltage for logic circuit | IOVCC | 2.5 | 2.8 | 3.6 | V |
| Input voltage 'H'level | VIH | 0.7*IOVCC | - | IOVCC | V |
| Input voltage 'L'level | VIL | GND | _ | 0.3*IOVCC | V |
| Output voltage 'Htlevel | VOH | 0.8*IOVCC | _ | IOVCC | V |
| Output voltage 'L'level | VOL | GND | _ | 0.2*IOVCC | V |

3.2 Backlight Characteristics

| | | | | | | 1a - 25C |
|-----------------|--------|-----|---------------|------|-------|-----------|
| Item | Symbol | Min | Тур | Max | Unit | Condition |
| Forward voltage | Vf | - | 12.8 | | V | If=40 |
| Luminance | LV | | 8000 | _ | cd/m | mA |
| Number of LED | - | | 4X2 | | Piece | - |
| Connection mode | S/P | | 4Serial/2Para | llel | - | - |

Using condition: constant current driving method If= 2×20 mA (+/-10%)

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 $T_{2} = 25C$

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4. TIMING CHARACTERISTICS

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4.1 Serial Interface Characteristics(3-line serial)

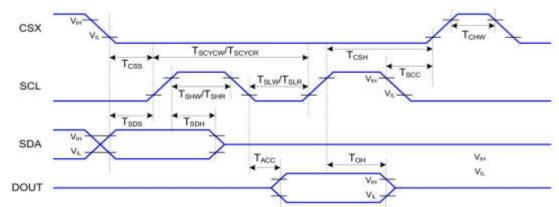


Figure 4.1 3-line serial Interface Timing Characteristics

Ta=25C

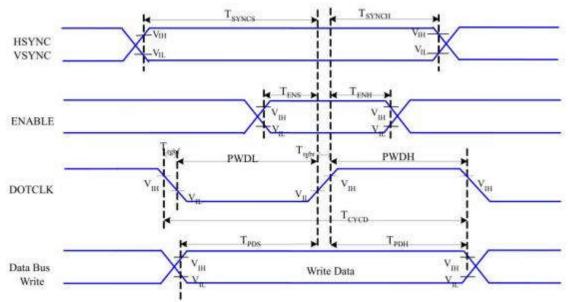
| IOVCC=1 | 1 65~3 7V | VCC=2 | 2 5~3 71 | Ι |
|---------|-----------|-------|----------|---|

| Signal | Symbol | Parameter | Min | Max | Unit | Description |
|--------|--------|-------------------------------|-----|-----|------|-------------|
| | TCSS | Chip select setup time(write) | 15 | - | ns | |
| | TCSH | Chip select hold time(write) | 15 | - | ns | |
| CSX | TCSS | Chip select setup time(read) | 60 | - | ns | |
| | TSCC | Chip Select hold time(read) | 60 | - | ns | |
| | TCHW | Chip Select 'H' pulse width | 40 | - | ns | |
| | TSCYCW | Serial clock cycle(write) | 66 | - | ns | Write |
| | TSHW | SCL 'H' pulse width(write) | 15 | - | ns | Command & |
| SCL | TSLW | SCL 'L' pulse width(write) | 15 | - | ns | Data Ram |
| SCL | TSCYCR | Serial clock cycle(read) | 150 | - | ns | Read |
| | TSHR | SCL 'H' pulse width(read) | 60 | - | ns | Command & |
| | TSLR | SCL 'L' pulse width(read) | 60 | - | ns | Data Ram |
| SDA | TSDS | Data setup time | 10 | - | ns | |
| (DIN) | TSDH | Data hold time | 10 | - | ns | |

Note : The rising time and falling time (Tr, Tf) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

Table 4.1 3-line Serial Interface Characteristics

4.2 RGB Interface Characteristics



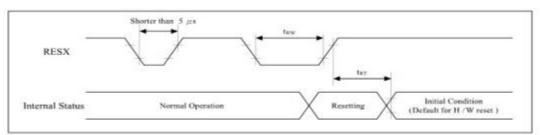


| | | | Та | =25C, I | OVCC= | =1.8V, VCC=2.8V |
|-----------------|-------------------------|-------------------------------|-----|---------|-------|-----------------|
| Signal | Symbol | Parameter | Min | Max | Unit | Description |
| HSYNC, VSYNC | TSYNCS | VSYNC,HSYNV Setup Time | 5 | - | ns | |
| ENABLE | TENS | Enable setup time | 5 | - | ns | |
| ENABLE | TENH | Enable hold time | 5 | - | ns | |
| | PWDH | DOTCLK High-level Pulse Width | 15 | - | ns | |
| | PWDL | DOTCLK Low-level Pulse Width | 15 | - | ns | |
| DOTCLK | TCYCD, | DOTCLK Cycle Time | 33 | - | ns | |
| | Trghr , Trghf | DOTCLK Rise/Fall time. | - | 15 | ns | |
| | TPDS | PD Data Setup Time | 5 | - | ns | |
| DB 💙 | TPDH | PD Data Hold Time | 5 | - | ns | |

 Table 4.2.2 18/16 Bits RGB Interface Timing Characteristics



5. Reset Timing



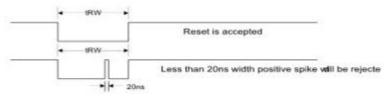
| Signal | Symbol | Parameter | Min. | Max. | Unit |
|--------|--------|----------------------|----------|-----------|------|
| | tenv | Reset pulse duration | 10(Note) | | us |
| RESX | 2011 | Reset cancel | | 10(Note) | ms |
| | Let | | | 120(Note) | ms |

Notes:

- 1. The reset cancel also includes required time for loading ID bytes, VCOM setting and other settings from EEPROM (or similar device) to registers.
- This loading is done every time when there is HW reset cancel time (**h**P) within 10 ms after a rising edge of RESX.
- 2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below :

| Action |
|----------------|
| Reset Rejected |
| Reset |
| Reset starts |
| |

- 3. During the Resetting period, the display will be blanked(The display is entering blanking sequence, which maximum time is 120 ms, when Reset
- Starts at Sleep-Out status. The display remains the blank state in Sleep-In mode). Then return to Default condition for Hardware Reset
- 4. Spike Rejection also applies during a valid reset pulse as shown below :

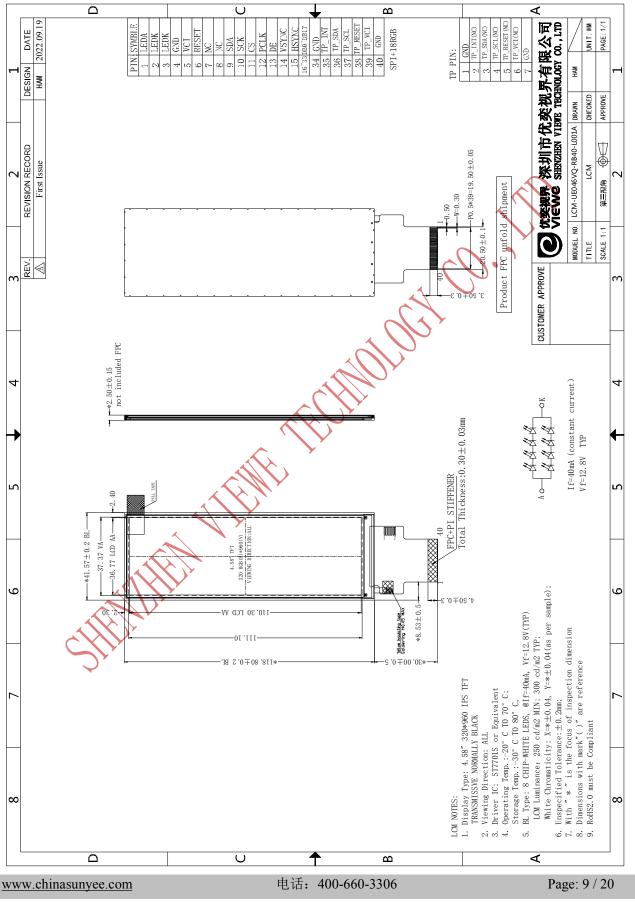


- 5. When Reset applied during Sleep-In Mode.
- 6. When Reset applied during Sleep-Out Mode
- 7. It is necessary to wait 10ms after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120 ms.



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6. EXTERNAL DIMENSIONS



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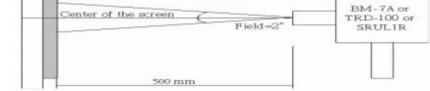
7. INTERFACE SIGNAL

| Pin No. | Symbol | Description | | | | |
|---------|----------|---|--|--|--|--|
| 1 | LEDA | LED backlight anode | | | | |
| 2 | LEDK | LED backlight cathode | | | | |
| 3 | LEDK | LED backlight cathode | | | | |
| 4 | GND | Power ground | | | | |
| 5 | VCC | Analog power supply | | | | |
| 6 | RESET | Reset signal(low active) | | | | |
| 7 | NC | NC | | | | |
| 8 | NC | NC | | | | |
| 9 | SDA | Serial data input pin in serial bus system interface | | | | |
| 10 | SCK | Pixel clock signal input pm | | | | |
| 11 | CS | Chip select | | | | |
| 12 | PCLK | Pixel clock signal in RGB interface | | | | |
| 13 | DE | Data enable signal in RGB I/F mode | | | | |
| 14 | VSYNC | Vertical synchronizing signal input pin | | | | |
| 15 | HSYNC | Horizontal synchronizing signal input pin | | | | |
| 16~33 | DB0~DB17 | RGB data signal (DB0:BLUE LSB;DB5:BLUE MSB;DB6:GREEN LSB;DB11:GREEN MSB;DB12:RED LSB;DB17:RED MSB) | | | | |
| 34 | GND | Power ground | | | | |
| 35 | TP_INT | INT pin for CTP | | | | |
| 36 | TP_SDA | SDA pin for CTP | | | | |
| 37 | TP_SCL | SCL pin for CTP | | | | |
| 38 | TP_RESET | Reset pin for CTP | | | | |
| 39 | TP_VCI | Power supply for CTP | | | | |
| 40 | GND | Power ground | | | | |



8. ELECTRO-OPTICAL CHARACTERISTICS

| Item | 1 | Sy | mbol | Condition | Min | Тур | Max | Unit | Note |
|--|-------|------------------------|---------|---|-------|-------|----------|--------|------|
| Transmittance (w/o polarizer) | | | Т% | | | 5.7 | - | % | 1 |
| Contrast ra | tio | | Cr | θ=0° | 1000 | 1200 | - | - | 3 |
| Response ti | me | Ton+Toff | | Ta=25C | - | 25 | 35 | ms | 4 |
| Surface Lumin | ance | | LV | | | 300 | | cd/m | 2 |
| | | | Θx+ | | | 80 | | deg | |
| Viewing angle | rongo | H or | Θx- | Center | | 80 | -> | deg | 5 |
| viewing angle | lange | V er | ΘY+ | CR>10 | | 80 | <u> </u> | deg | |
| | | | ΘΥ- | | Z | 80 | - | deg | |
| | | | x | | 0.627 | 0.647 | 0.667 | - | |
| | Red | | у | | 0,292 | 0.312 | 0.332 | - | |
| | | | x | Viewing | 0.241 | 0.261 | 0.281 | - | |
| CIE(x,y) | Green | | у 🎸 | normal angle | 0.545 | 0.565 | 0.585 | - | 6 |
| chromaticity | | | X | $\Theta x = \theta y = 0^{\circ}$ | 0.117 | 0.137 | 0.157 | - | - 6 |
| | Blue | \mathbf{x} | у | Ta=25C | 0.090 | 0.110 | 0.130 | - | |
| | | | X | | 0.27 | 0.30 | 0.33 | - | |
| | White | | у | | 0.29 | 0.33 | 0.36 | - | |
| ote 1.Ambient of Measure device Ly = Ave | BM-7A | (TOF e Lur - Opt | CON), v | iewing cone=2° ith all white pixe (x.y) tule | | | | • Note | 1 |

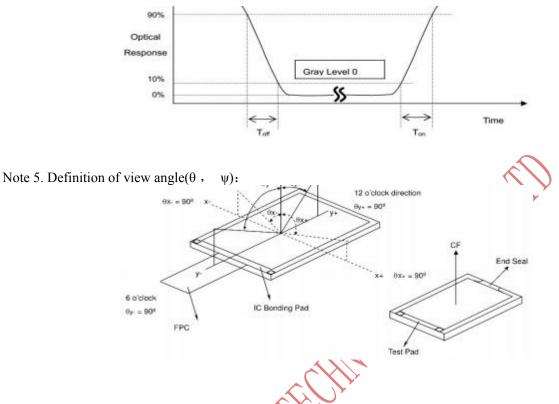


Note 3. Definition of Contrast Ratio:

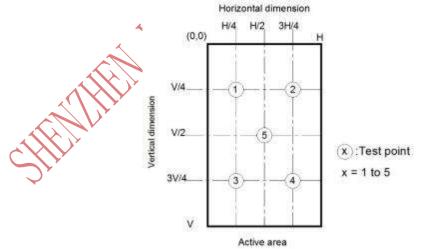
Average Surface Luminance with all black pixels (P1,P2,P3,P4,P5) CR = Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)



Note 4. Definition of Response Time (Ton, Toff), The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 6. The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room. Measouring method for Contrast ratio, surface luminance, Luminance uniformity, CIE(x,y) chromaticity.



Light spot size \Box =7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-7



9. RELIABILITY

| Item 项目 | Test Condition 测试条件 | Remark 备注 |
|--|---|--------------|
| High Temperature Storage | Ta =+80°C / 96Hours | Note1,2,3 |
| Low Temperature Storage | Ta =-30°C / 96Hours | Note1,2,3 |
| High Temperature Operating | Ta =+70°C / 96Hours | Note1,2,3 |
| Low Temperature Operating | Ta =-20°C / 96Hours | Note1,2,3 |
| Temperature Cycle storage Test | -30°C/30min Δ+70°C /30min for 30cycles, Fransfer time less than 5min | Note2,3 |
| Thermal humidity storage Test | 80°C x 90%RH / 96Hours | Note2,3 |
| Package Vibration Test | Frequency: 10Hz~55Hz, Amplitude:1.5mm, 1 hrs for each direction of X, Y, Z | Note2 |
| Packing shock test | Drop to the ground from 60cm height, 1 corner, 3 edges, 6 surfaces. | Note2 |
| ESD test | Contact: ±4KV Air: ±8KV | ESD |
| Inspection after Test: Note1:Ta is the ambient temperature of sam | ples. | |

Note 2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification. Note 3: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.



10. INSPECTION CRITERION

This specification is made to be used as the standard acceptance/rejection criteria for Normal LCM Product. **1 Sample plan**

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-

1993,normal level 2 and based on: Major defect: AQL 0.65 Minor defect: AQL 1.0

2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25°C and normal humidity 60±15%RH).

3. Definition of inspection zone in LCD.

Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area) Zone C: Outside viewing area (invisible area after assembly in customer's product) Fig.1 Inspection zones in an LCD. Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

А

4.Standards of inspection items

| Item No | Items to be inspected | Inspection Standard | Classification of defects |
|---------|--------------------------|--|------------------------------|
| | | 1.No display | |
| C | | 2.Display abnormally | |
| 4.1.1 | All functional | 3. Missing vertical, horizontal segment | |
| 4.1.1 | defects | 4.Short circuit | |
| | | 5. Back-light no lighting, flickering and | Major |
| | | abnormal lighting | |
| 4.1.2 | Missing | Missing component | |
| 4.1.0 | Outline | Overall outline dimension beyond the drawing | |
| 4.1.3 | dimension | is not allowed. | |
| 4.1.4 | linearity | No more than 1.5% | |



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4.2 Cosmetic Defect

| Item No | Items to be inspected | Ins | Classification of defects | | | |
|------------|---|--|------------------------------|---------------|------------------|-------|
| | Clear | For dark/white spot defined as $\Phi = (x + y)$ | | | Q x x ↓ | |
| | Spots Black and white Spot defect | 1 | Ac | cceptable Qty | ý | |
| | Pinhole, Foreign | Zone Size(mm) | А | В | C | Minor |
| | Particle, polarizer | Ф≤0. 15 | Igno | | | |
| | Dirt | $0.15 < \Phi \le 0.20$ 0.20 < $\Phi \le 0.30$ | | 2 | - Ignore | |
| | | $\Phi > 0.30$ | | 1 | (0) | |
| | Clear Spots TP Dirt | 2 | | 4 | | |
| | | Acceptable Qty | | | | |
| 4.2.1 | | Zone Size(mm) | A | В | C | - |
| | | Ф≤0. 15 | Ign | ore | | Minor |
| | | 0.15<Φ≤0.20 | 2 | | Ignore | |
| | | 0.20<Φ≤0.30 | ▶ | 1 | | |
| | | Φ>0.30 | 0 | | | |
| | | 3 | | | | |
| | Dim Spots | Zone | Acceptable Qty | | y | |
| | Circle shaped and dim edged defects | Size(mm) | А | В | C | |
| | | Φ≤0.2 Ignore | | ore | | Minor |
| C | | 0.20<Φ≤0.40 | | 2 | Ignore | |
| | | 0.40<Φ≤0.60 | | 1 | | |
| | | $\Phi > 0.60$ | (|) | | |



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| Item No | Items to be inspected | Inspection Standard | | | | | Classification of defects |
|--------------|----------------------------|---------------------------------------|--|--------------|----------|--------|------------------------------|
| | Line | Siz | ze(mm) | Acce | ptabl | le Qty | |
| | defect | L (L an ath) | W/(W/: 44L) | | Zor | ne | |
| | Black line, White line, | L(Length) | W(Width) | Α | В | C | |
| | Foreign material | Ignore | W≤0.05 | Igno | re | | Minor |
| | on | L ≤5.0 | $0.05 < W \le 0.08$ | 2 | | Ignore | |
| | polarizer | | W>0.08 | 0 | | | |
| 4.2.2 | | The line can operating cor | be seen after mobile | phone | in the | 2 | |
| | Foreign | Siz | ze(mm) | Acce | ptab | e Qty | |
| | material | L(Length) | W(Width) | A | Zor B | C | Minor |
| | TP film | Ignore | W≤0.05 | Igno | re | | |
| | | L ≤5.0 | 0.05 <w≤0.08< td=""><td>3</td><td></td><td>Ignore</td><td></td></w≤0.08<> | 3 | | Ignore | |
| | | | W>0.08 | 0 | | | |
| | Dim line | by the line de only in | bling or in the operat effect of 4.2.2. If the s g condition or some following. | scratch | can b | e seen | |
| | defect Polarizer | Siz | ze(mm) | Acce | ptabl | le Qty | |
| 4.2.3 | scratch TP film | L(Length) | W(Width) | | Zor | ne | Minor |
| | scratch | | w(widdi) | Α | В | C | |
| | X X YY | Ignore | W≤0.03 | Igno | re | | |
| | | 5.0< L≤10.0 | $0.03 \le W \le 0.05$ | 2 | | Ignore | |
| | | L≤5.0 | 0.05 <w≤0.08< td=""><td>1</td><td></td><td></td><td></td></w≤0.08<> | 1 | | | |
| \checkmark | | | W>0.08 | 0 | | | |
| | | Air bubbles between glass & polarizer | | | | | |
| 4.2.4 | Polarize | Zone | | Acceptable Q | | le Qty | |
| | | Size(mm) | | Α | В | C | Minor |
| | Air bubble | Ф≤0.20 | Φ≤0.20 | | Ignore | | |
| | | 0.20<Φ≤0.3 | | 2 | | Ignore | |
| | | Φ>0.30 | 0 | | | | |



| 4.35 Glass defect $ \begin{array}{c c c c c c c c c c c c c c c c c c c $ | Item No | Items to be inspected | | Inspection Standard | | | | |
|---|------------|--------------------------|-----------------------------------|--|------------------|-------|--|--|
| 4.35 $Glass defect$ $4.35 Glass defect$ $(i)Usual surface cracks A:LCD Glass defect$ $X(mm) Y(mm) Z(mm) \\ \leq 3.0 (intermediate border line of the seal birth of the seal$ | | | (i) Chips on co A:LCD Glass | orner defect | | | | |
| 4.35 G_{135} S_{10} S_{1 | | | X(mm) | Y(mm) | Z(mm) | | | |
| 4.35 Glass defect $X(mm) \qquad Y(mm) \qquad Z(mm) \\ \leq 3.0 \qquad (ii)Usual surface cracks \\ A:LCD Glass defect \\ \hline X(mm) \qquad Y(mm) \qquad Z(mm) \\ \leq 3.0 \qquad (iii)Usual surface cracks \\ A:LCD Glass defect \\ \hline X(mm) \qquad Y(mm) \qquad Z(mm) \\ \leq 3.0 \qquad (iii)Usual surface cracks \\ A:LCD Glass defect \\ \hline X(mm) \qquad Y(mm) \qquad Z(mm) \\ \leq 3.0 \qquad (iiner border line of the seal \\ \hline Disregard \\ \hline B:TP Glass defect \\ \hline $ | | | | | | | | |
| 4.35 Glass defect $ \begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | the corner of t into the ITO p | erminal shall not be a ad or expose perimeter | llowed to extend | | | |
| 4.35 Glass defect $ \begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | X(mm) | Y(mm) | Z(mm) | - | | |
| 4.35 Glass defect $(ii)Usual surface eracks$ A:LCD Glass defect $Minor$ X(mm) $Y(mm)$ $Z(mm)\leq 3.0 \leq 100 MinorS:TP$ Glass defect $MinorX(mm)$ $Y(mm)$ $Z(mm)B:TP$ Glass defect $MinorX(mm)$ $Y(mm)$ $Z(mm)\leq 6.0 \leq 2.0 Minor$ | | | | | | - | | |
| $ \begin{array}{ c c c c c c c c c c c c c c c c c c c$ | 4.35 | defect | ALCO Gass | | * The second | Minor | | |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | X(mm) | Y(mm) | Z(mm) | - | | |
| X(mm)Y(mm)Z(mm) ≤ 6.0 < 2.0 Disregard(iii) Crack | | - THE | | <inner border<="" td=""><td></td><td></td></inner> | | | | |
| ≤6.0 <2.0 Disregard (iii) Crack | Ċ | | B:TP Glass de | efect | TT | | | |
| (iii) Crack | | | X(mm) | Y(mm) | Z(mm) | - | | |
| (iii) Crack | | | ≤6.0 | <2.0 | Disregard | | | |
| | | | (iii) Crack | | | | | |
| crack | | | cra | | | | | |

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11. PRECAUTIONS FOR USE OF LCD MODULES

1. Handling Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the VO cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.

(12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your

body have the same electric potential. Be sure to ground the body when handling the LCD modules.

- Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor. - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated

- (13)Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.

2. Storage precautions

- 2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0C \sim 40C$ Relatively humidity: $\leq 80\%$

- 2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 2.4 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.



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12. PACKAGE DRAWING

