

LCD MODULE
SPECIFICATION

Model:	UE055FH-RA40-A027-C
Version:	V1.1
Date:	20190425

☒ Preliminary Specification 样品规格书

☐ Final Specification 量产规格书

Customer Confirmation 客户确认

Approved by	Notes

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VIEWE Confirmation 优奕确认

Prepared by	Reviewed by	Approved by

REVISION HISTORY

Revision 版本号	Date 日期	Contents of Revision Change 修改内容	Remark 备注
V1.0	2018.10.16	Preliminary release	
V1.1	2019.4.25	Modify the TP thickness and update the a thickness to 2.57. TP cancels the light guide f changes the key character to mirror silver	ALL

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1. GENERAL INFORMATION

1.1 Features

- 1) Pixel Arrangement: RGB Vertical Stripe
- 2) Interface Mode: MIPI
- 3) Driver IC: OTM1901A-C5
- 4) Operation Temperature: -20~70℃
- 5) Storage Temperature: -20~70℃
- 6) Backlight Type: White LED
- 7) Display mode: LTPS Normally Black,
- 8) Pixel Density: 400 PPI
- 9) LED life time: 30,000 Hours

1.2 Mechanical Specification

Item 项目	Specification 规格	Unit 单位	Remark 备注
Pixel Driving element	LTPS TFT	-	
Screen Size	5.5	Inch	Diagonal
Resolution	1080(W)*3(RGB)*1920(H)	Dots	
Interface	MIPI	-	4lane
Module Power Consumption	0.95	Watt	Typ.
Active Area	68.04(W)*120.96(H)	mm	
VActive VArea	68.64(W)*121.56(H)	mm	
Pixel pitch (W*H)	0.063(W)*0.063(H)	mm	
Module Size (W*H*D)_CTP	73.80(W)*150.80(H)*2.57(D)	mm	Tolerance: ±0.2
Luminance	360	cd/m ²	Typ.
Viewing Direction	All	O'clock	-
Display Color	16.7M	Colors	24bits

2. ABSOLUTE MAXIMUM RATINGS

Item 项目	Symbol 符号	Min. 最小值	Max. 最大值	Unit 单位	Remark 备注
Power supply1 voltage	IOVCC	-0.3	3.6	V	Note1
Power supply2 voltage	AVDD	-0.3	6.6	V	Note1
Power supply3 voltage	AVEE	-6.6	0.3	V	Note1
Power supply1 voltage	TP_IOVCC	-0.3	3.6	V	
LED forward current	I _F	-0.001	30	mA	For each led,Note1
LED Reverse Voltage	V _R	-	5	V	For each led,Note1
Operating temperature	T _{op}	-20	70	°C	Note1,2
Storage temperature	T _{st}	-20	70	°C	Note1,2
Humidity	H _{st}	10	90	%RH	Note1,3

(Ta=+25°C,GND=0V)

Note1:If the module exceeds the absolute maximum ratings, it may be damaged permanently. Also if the module operates with the absolute maximum ratings for a long time, the reliability may drop.

Note2: In case of temperature below 0°C,the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one.

Note3: Temp. ≤ 60°C , 90% RH MAX.

Temp. > 60°C , Absolute humidity shall be less than 90% RH .

3. MECHANICAL DRAWING

[illegible]

4. I/O CONNECTION & BLOCK DIAGRAM

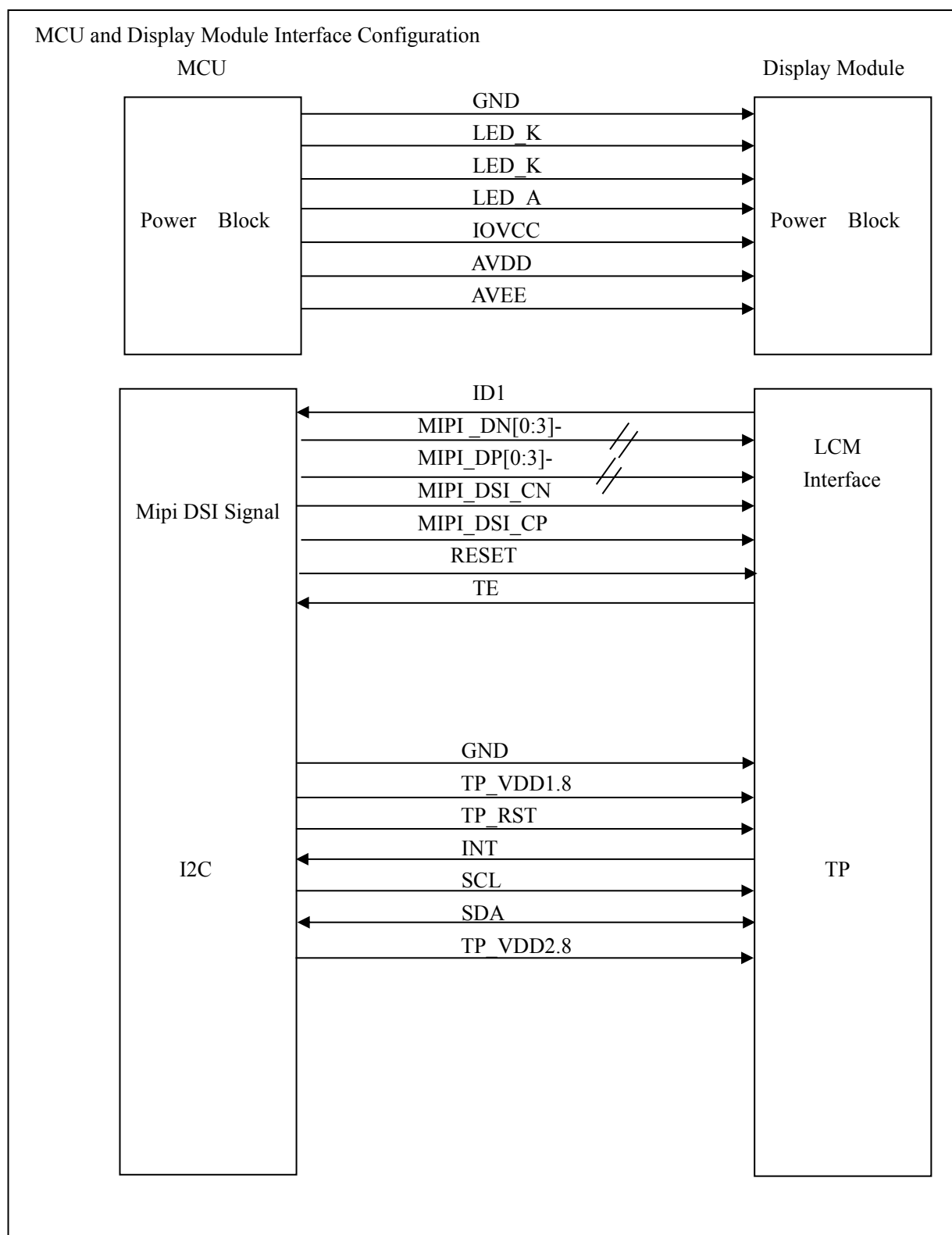
4.1 I/O Connection

Pin No. 序号	Symbol 符号	I/O	Description 描述
1	GND	P	Power Ground
2	MIPI_D3N	I	MIPI-DSI data lane 3 negative-end input pin
3	MIPI_D3P	I	MIPI-DSI data lane 3 positive -end input pin
4	GND	P	Power Ground
5	MIPI_D0N	I	MIPI-DSI data lane 0 negative-end input pin
6	MIPI_D0P	I	MIPI-DSI data lane 0 positive -end input pin
7	GND	P	Power Ground
8	MIPI_CN	I	MIPI-DSI clock lane negative-end input pin
9	MIPI_CP	I	MIPI-DSI clock lane positive-end input pin
10	GND	P	Power Ground
11	MIPI_D1N	I	MIPI-DSI data lane 1 negative-end input pin
12	MIPI_D1P	I	MIPI-DSI data lane 1 positive -end input pin
13	GND	P	Power Ground
14	MIPI_D2N	I	MIPI-DSI data lane 2 negative-end input pin
15	MIPI_D2P	I	MIPI-DSI data lane 2 positive -end input pin
16	GND	P	Power Ground
17	NC	-	Dummy
18	ID1		LCM Hardware ID Selection Default pull low
19-22	NC	-	Dummy
23	TE	O	Tearing effect output pin to synchronize MCU to frame writing
24-25	NC	-	Dummy
26	RESET	I	Chip reset signal
27	IOVCC	I	Power supply for digital circuits and IO pads(1.8V)
28	GND	P	Power Ground
29	AVDD	P	Positive Voltage for source driver
30	NC	-	Dummy
31	AVEE	P	Negative Voltage for source driver
32	GND	P	Power Ground
33-34	NC	-	Dummy
35-36	LED_K	P	Power supply for LED-
37	GND	P	Power Ground
38-39	LED_A	P	Power supply for LED+
40	GND	P	Power Ground

TP PIN	Symbol	I/O	Description
1-3	GND	P	Ground.
4	TP-VDD1.8V	P	TP_VDD(1.8V) Power Supply for TP
5	GND	P	Ground.
6	TP_RST	I	The signal will reset the TP, Signal is active low
7	INT	O	Interrupt signals for TP
8	SCL	I	I2C clock signals for TP
9	SDA	I/O	I2C data signals for TP
10	GND	P	Ground.

I: Input; O: Output; P: Power

4.2 Block Diagram



5. ELECTRICAL CHARACTERISTICS

5.1 TFT-LCD Panel Driving Section

Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Power Supply1 Voltage	IOVCC	1.7	1.8	3.6	V	-
Power Supply2 Voltage	AVDD	4.5	5	6	V	-
Power Supply3 Voltage	AVEE	-6	-5	0	V	-
Power Supply4 Voltage	TP_IOVDD	1.7	1.8	3.6	V	-
Power Supply Current	IioVCC	-	30	-	mA	Note1
Logic Input High Voltage	V _{IH}	0.7IOVCC	-	IOVCC	V	-
Logic Input Low Voltage	V _{IL}	0	-	0.3IOVCC	V	-
Panel Power Consumption	P _{IOVCC}	-	0.054	-	Watt	Note1
Module Power Consumption	P _{LCM}	-	0.95	-	Watt	Note1,2

(Ta=+25°C,GND=0V)

Note1: Measurement Conditions (Video Mode): Full Screen Red Pattern, VDD=2.8V, 60Hz Refresh.

Note2: P_{LCM}= P_{VDD}+ P_{B/L}, About P_{B/L} information, inference to 5.2 Back Light Driving Section.

5.2 Back Light Driving Section

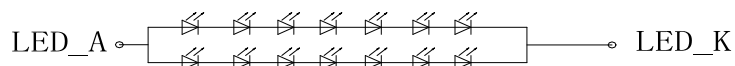
Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Forward Voltage	V _F	-	22.4	-	V	Note1
Forward Current	I _F	-	40	-	mA	Note1
Backlight Power consumption	P _{B/L}	-	0.896	-	Watt	Note1
LED life time	-	30000	-	-	Hours	Note2
LED Quantity		14			PCS	

(Ta=+25°C,GND=0V)

Note1: The LED driving condition is defined for each LED module (7 LED Serial, 2 LED Parallel).

For each LED : I_F=20mA, V_F=3.2V(Typ.), Ta=25°C。

Note2: The “LED life time” is defined as the module brightness decrease to 50% of original brightness at I_{LED}=20mA(Per Led). The LED life time could be decreased if operating I_{LED} is larger than 20mA.



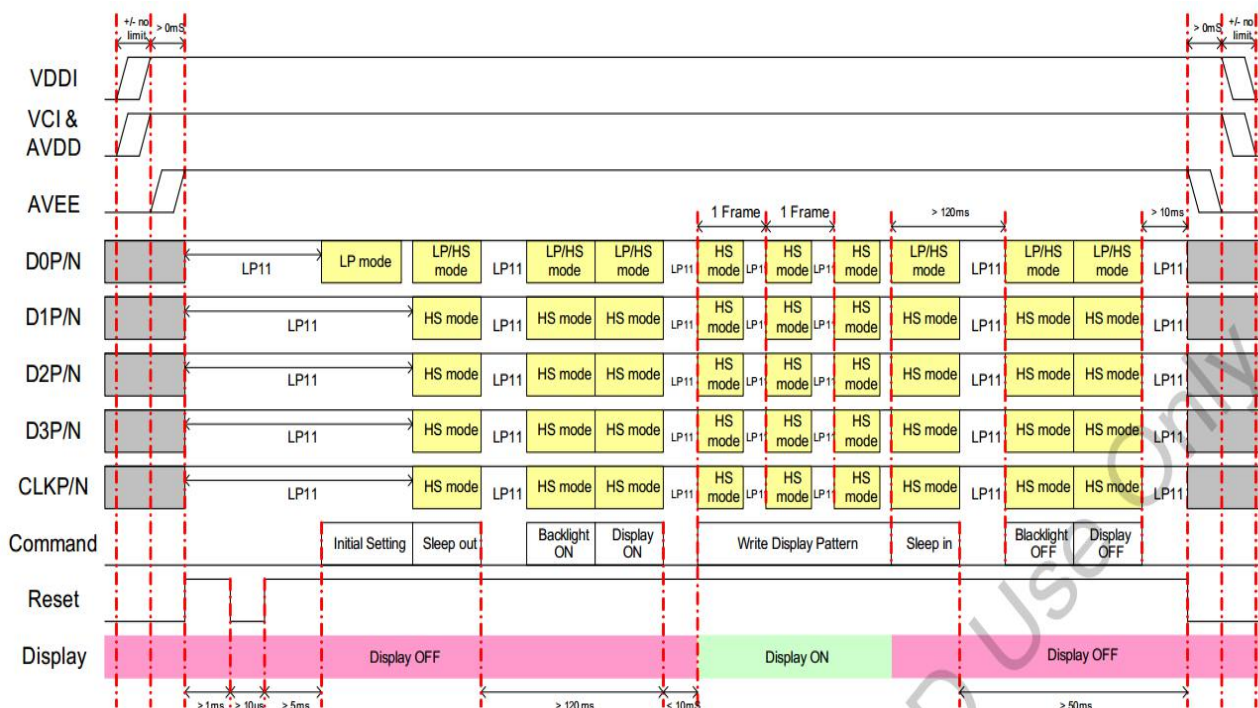
BACKLIGHT CIRCUIT DIAGRAM 20mA/LED (14LED)

LED V_f: 22.4V Typ

背光电路图

5.3 Power On/Off Sequence

5.3.1 Power On Sequence



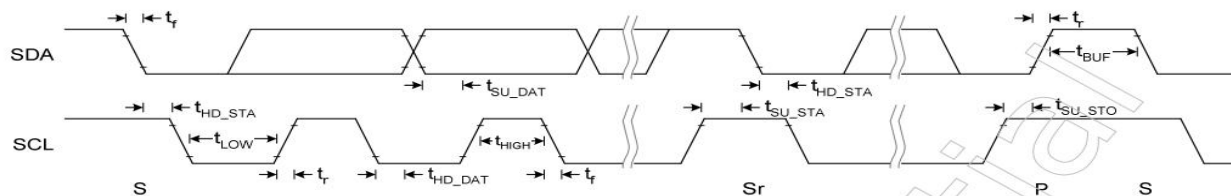
5.3.2 Power Off Sequence

The uncontrolled power off means a situation when e.g. there is removed a battery without the controlled power off sequence. There will not be any damages for the display module or the display module will not cause any damages for the host or lines of the interface.

At an uncontrolled power off the display will go blank and there will not be any visible effects within (TBD) second on the display (blank display) and remains blank until "Power On Sequence" powers it up.

5.4 Timing Characteristics

5.4.0 I2C interface timings

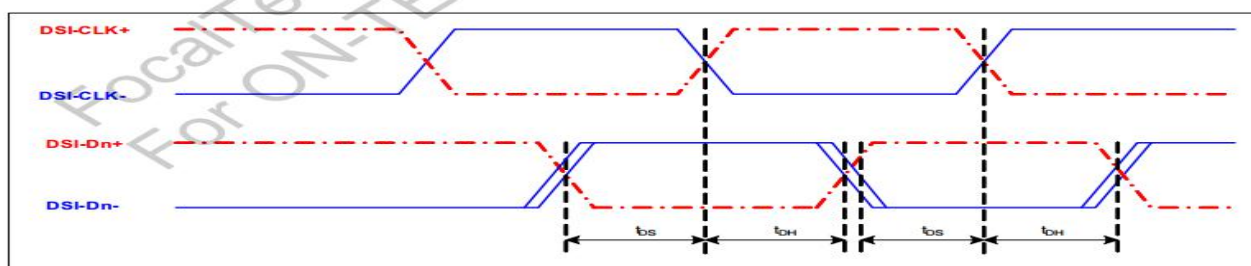
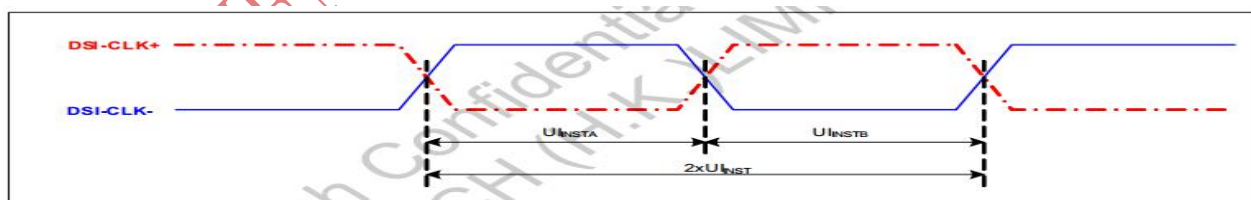


Parameter	Symbol	Min.	Max.	Unit
SCL low period	t_{lo}	1.3	-	us
SCL high period	t_{hi}	0.6	-	us
SCL setup time for START condition	t_{st1}	0.6	-	us
SCL setup time for STOP condition	t_{st3}	0.6	-	us
SCL hold time for START condition	t_{hd1}	0.6	-	us
SDA setup time	t_{st2}	0.1	-	us
SDA hold time	t_{hd2}	0	-	us

Note: 1.8V 通讯接口, 400Kbps 通讯速度, 上拉电阻 2K

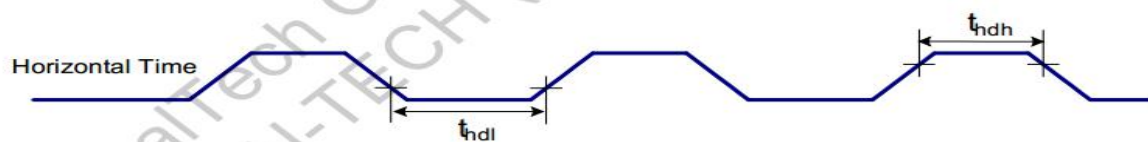
5.4.1 AC Characteristics

Parameter	Symbol	Parameter	Specification			Unit
			MIN	TYP	MAX	
High Speed Mode						
DSI-CLK+/-	2xUI _{INST}	Double UI instantaneous	2	-	8	ns
DSI-CLK+/-	UI _{INSTA} , UI _{INSTB}	UI instantaneous Halfs	1	-	4	ns
DSI-Dn+/-	t _{DS}	Data to clock setup time	0.15	-	-	UI
DSI-Dn+/-	t _{DH}	Data to clock hold time	0.15	-	-	UI
DSI-CLK+/-	t _{DRTCLK}	Differential rise time for clock	150	-	0.3UI	ps
DSI-Dn+/-	t _{DRTDATA}	Differential rise time for data	150	-	0.3UI	ps
DSI-CLK+/-	t _{DFTCLK}	Differential fall time for clock	150	-	0.3UI	ps
DSI-Dn+/-	t _{DFTDATA}	Differential fall time for data	150	-	0.3UI	ps

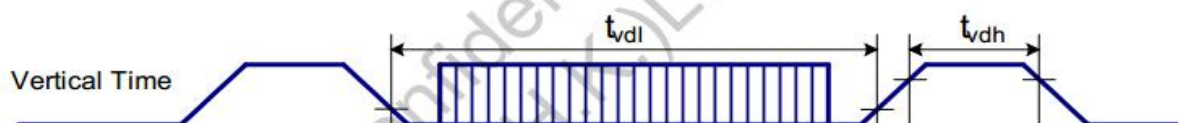


5.5 Timing Diagram

5.5.1 Horizontal Timings

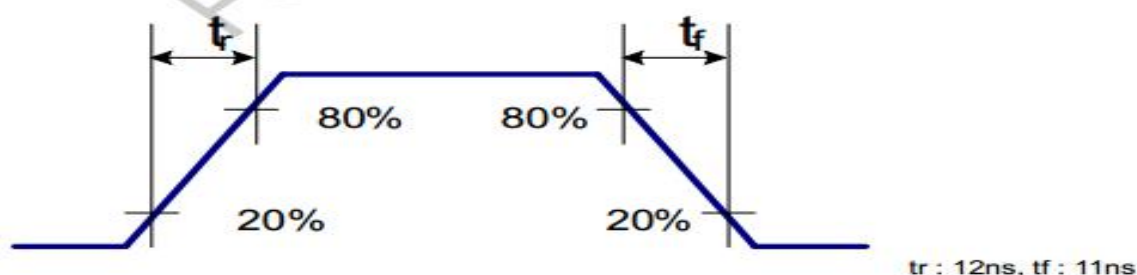


5.5.2 Vertical Timings



Idle Mode Off/On

The TE signal rising and falling timing is described below:



5.5.3 Timing Parameters

Parameter	Symbol	Value			Unit
		Min.	Typ.	MAX.	
Bit rate per lane	BRPHY	200	-	1000	Mbps
Active pixel per line	HACT	-	1080	-	Pixels
Horizontal back porch	t _{HBP}	-	30	-	DCLK
Horizontal sync active	t _{HSA}	-	2	-	DCLK
Horizontal front porch	t _{HFP}	-	15	-	DCLK
Active pixel per frame	VACT	-	1920	-	H
Vertical back porch	t _{VBP}	-	30	-	H
Vertical sync active	t _{VSA}	-	2	-	H
Vertical front porch	t _{VFP}	-	8	-	H

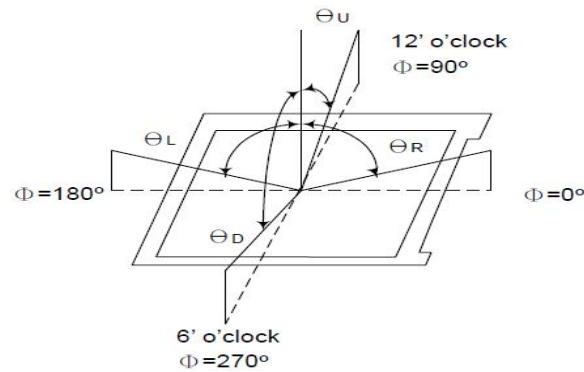
6. OPTICAL CHARACTERISTICS

Parameter 参数	Symbol 符号	Condition 条件	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注
Contrast Ratio	C/R	$\theta = 0^\circ$	1000	1300	-	-	Note(4)
NTSC Ratio	S	$\theta = 0^\circ$	65	70	-	%	Note(7)
Luminance	L	$\theta = 0^\circ$	-	360	-	cd/m ²	Note(5)
Luminance uniformity	U _W	$\theta = 0^\circ$	80	-	-	%	Note(3)
Response Time	T _R + T _F	25 °C	-	25	35	ms	Note(2)
Color Coordination	W _X	$\theta = 0^\circ$ (Center) Normal viewing angle B/L On	-0.03	0.288	+0.03	NTSC (x,y)	Note(6)
	W _Y			0.313			
	R _X			0.638			
	R _Y			0.335			
	G _X			0.289			
	G _Y			0.608			
	B _X			0.148			
	B _Y			0.058			
Viewing Angle	θ_L	C/R>10	-	80	-	Degree	Note(1)
	θ_R		-	80	-		
	θ_U		-	80	-		
	θ_D		-	80	-		

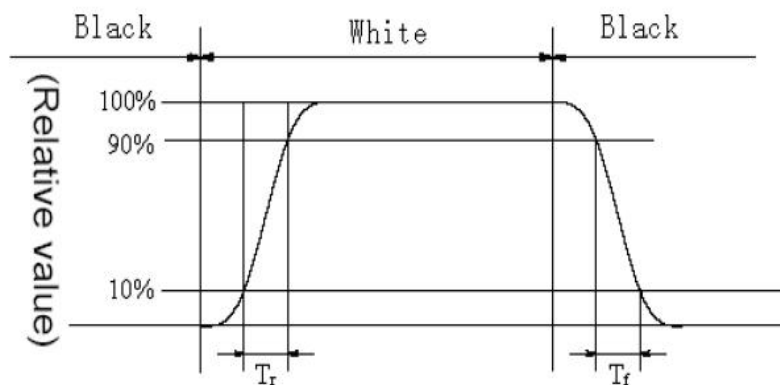
Test Conditions:

1. IOVCC=1.8V, I_F=40mA (Backlight current), the ambient temperature is +25°C.
2. The test systems refer to Note 8.

Note1: Definition of Viewing Angle: The viewing angle range that the CR>10

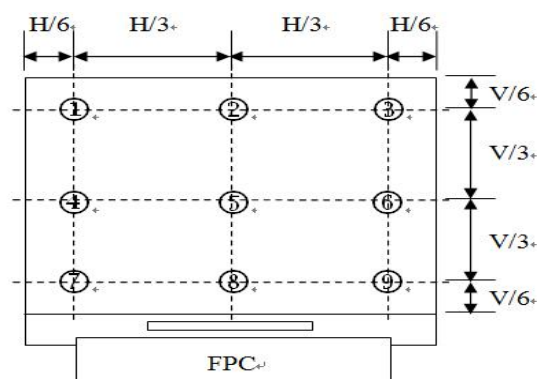


Note2: Definition of Response time: Sum of T_R and T_F



Note 3: Definition of Luminance Uniformity: Active area is divided into 9 measuring areas, every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$



Note4: Definition of Contrast Ratio (CR): measured at the center point of panel

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

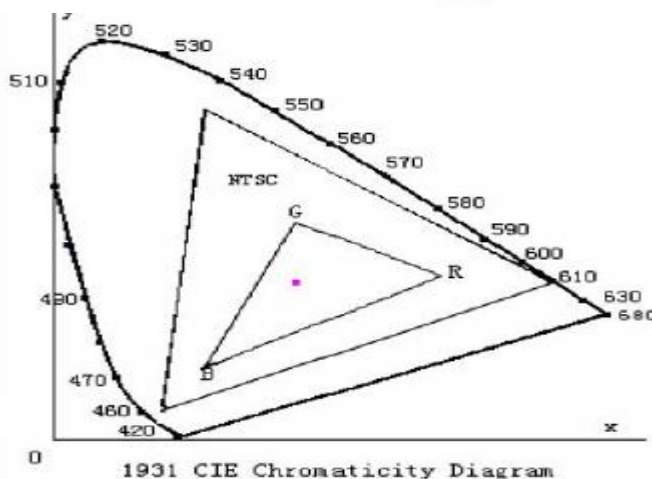
Note 5: Definition of Luminance: Center Luminance of white is defined as luminance values of 1 point average across the LCD surface.

Note 6: Definition of Color Chromaticity (CIE 1931)

Color coordinates of white & red, green, blue measured at center point of LCD.

Note 7: Definition of NTSC ratio:

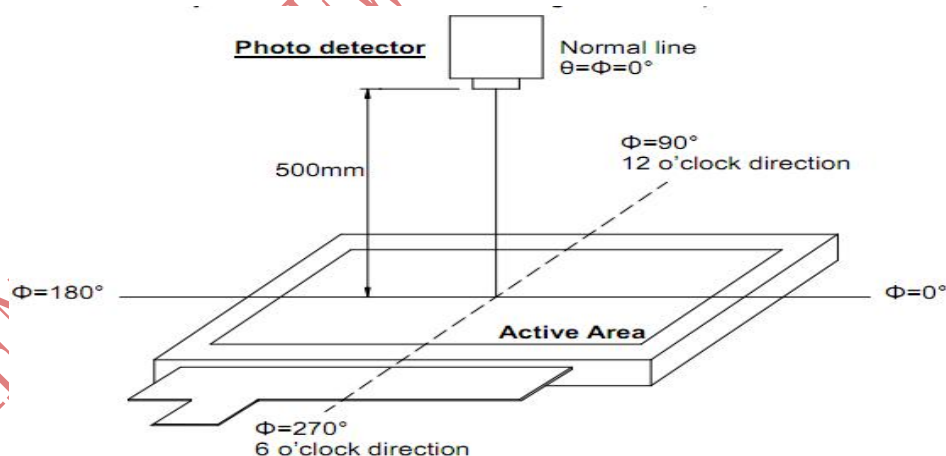
$$\text{NTSC ratio} = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}}$$



Note 8: Definition of measurement system.

optical

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, Field of view: 1°/Height: 500mm.)



7. RELIABILITY

Item 项目	Test Condition 测试条件	Remark 备注
High Temperature Storage	Ta =+70°C / 240Hours	Note1,2,3
Low Temperature Storage	Ta =-20°C / 240Hours	Note1,2,3
High Temperature Operating	Ta =+70°C / 240Hours	Note1,2,3
Low Temperature Operating	Ta =-20°C / 240Hours	Note1,2,3
Temperature Cycle storage Test	-20°C/30min ↔ +70°C /30min for 30cycles, Transfer time less than 5min	Note2,3
Thermal humidity storage Test	70°C x 90%RH / 240Hours	Note2,3
Package Vibration Test	Frequency: 10Hz~55Hz, Amplitude: 1.5mm, 1 hrs for each direction of X, Y, Z	Note2
Packing shock test	Drop to the ground from 60cm height, 1 corner, 3 edges, 6 surfaces.	Note2

Inspection after Test:

Note1: Ta is the ambient temperature of samples.

Note 2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.

Note 3: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

Note5: The final interpretation of the above reliability test standard is owned by VIEWE.

8. PACKAGE DRAWING

